

Fig. 1

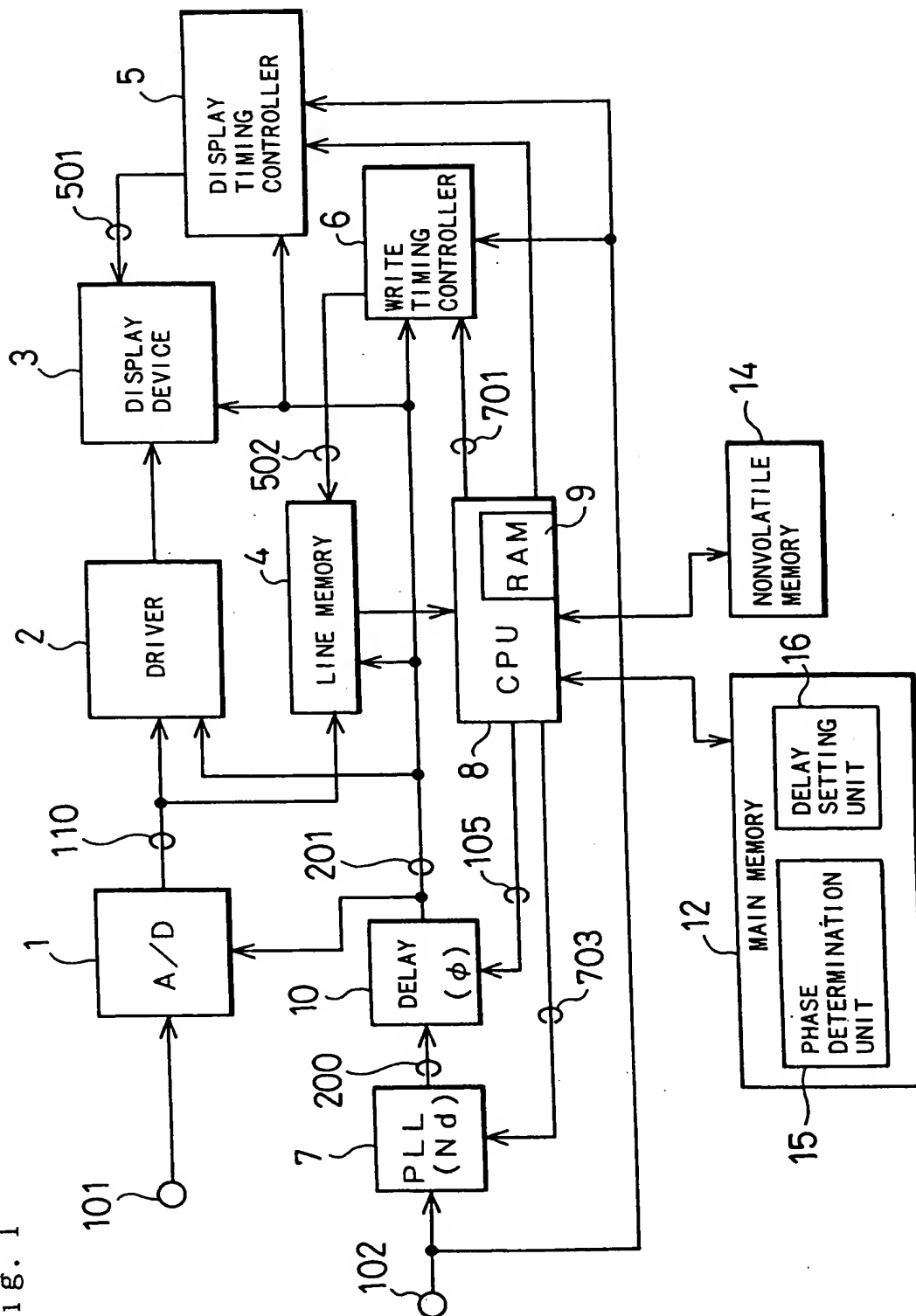


Fig. 2

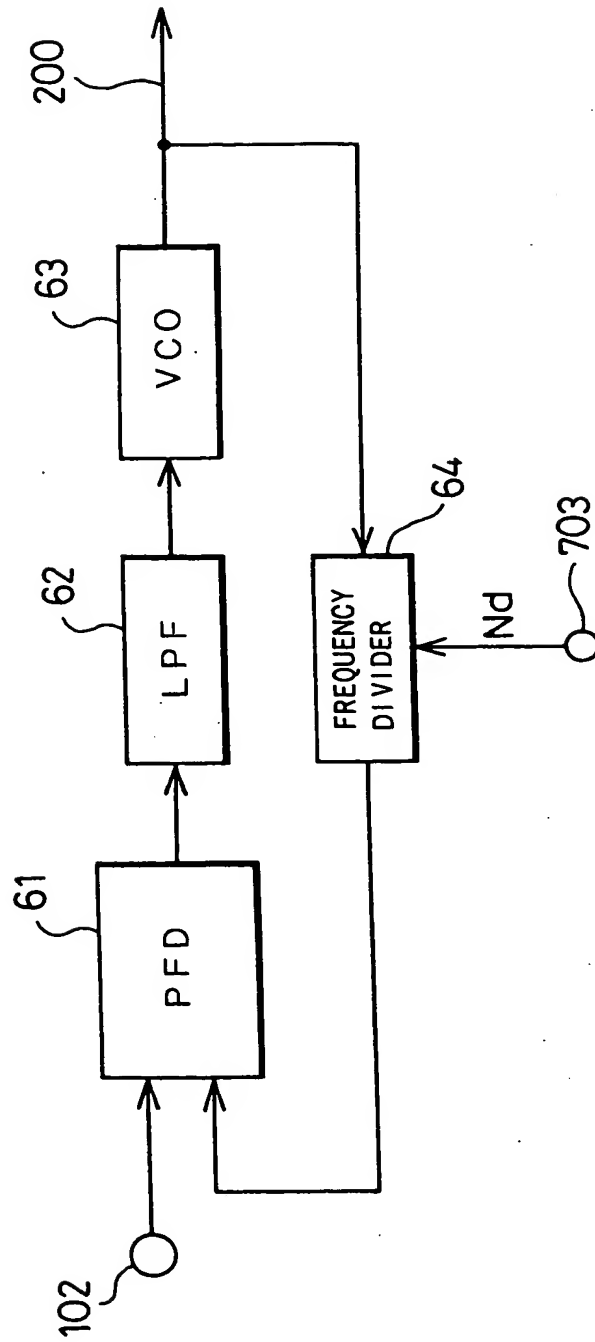


Fig. 3

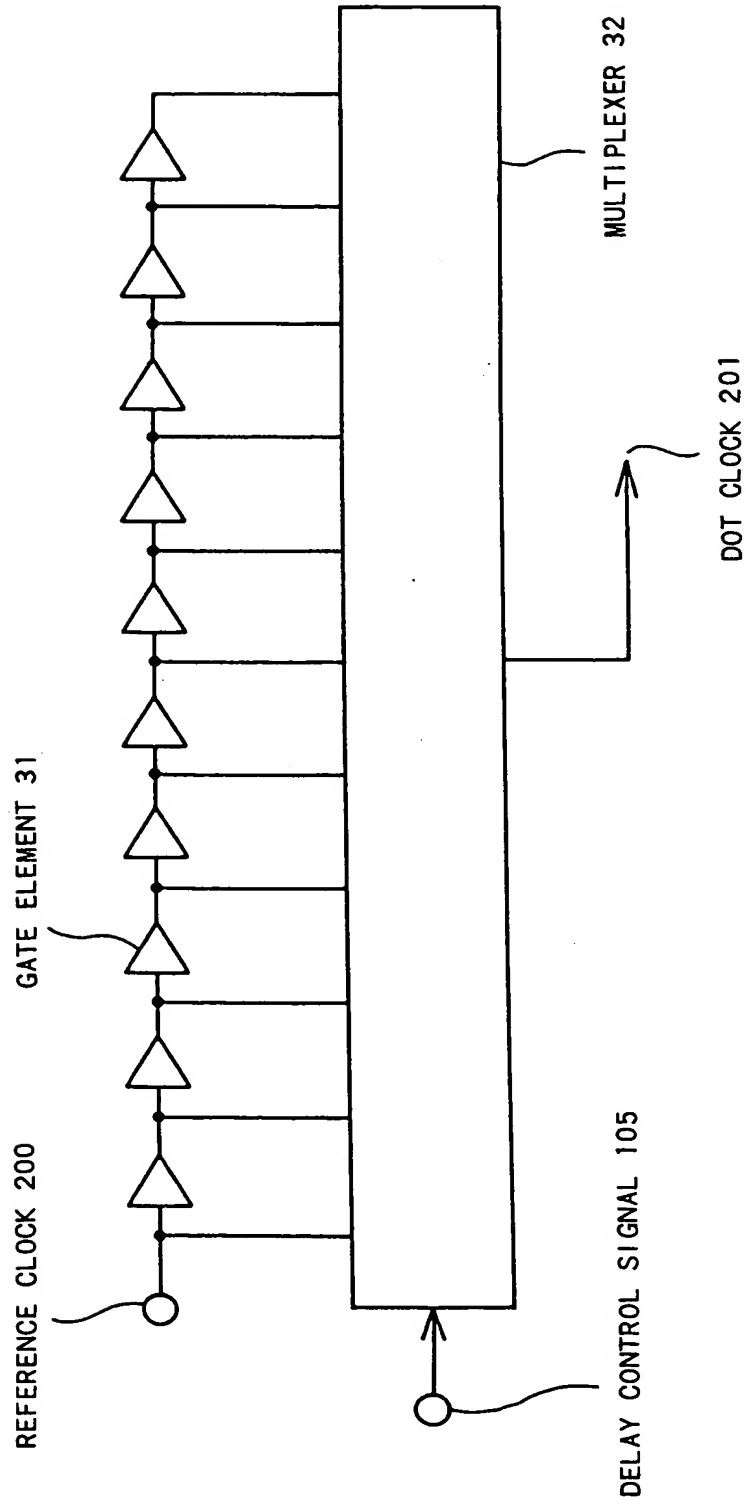


Fig. 4

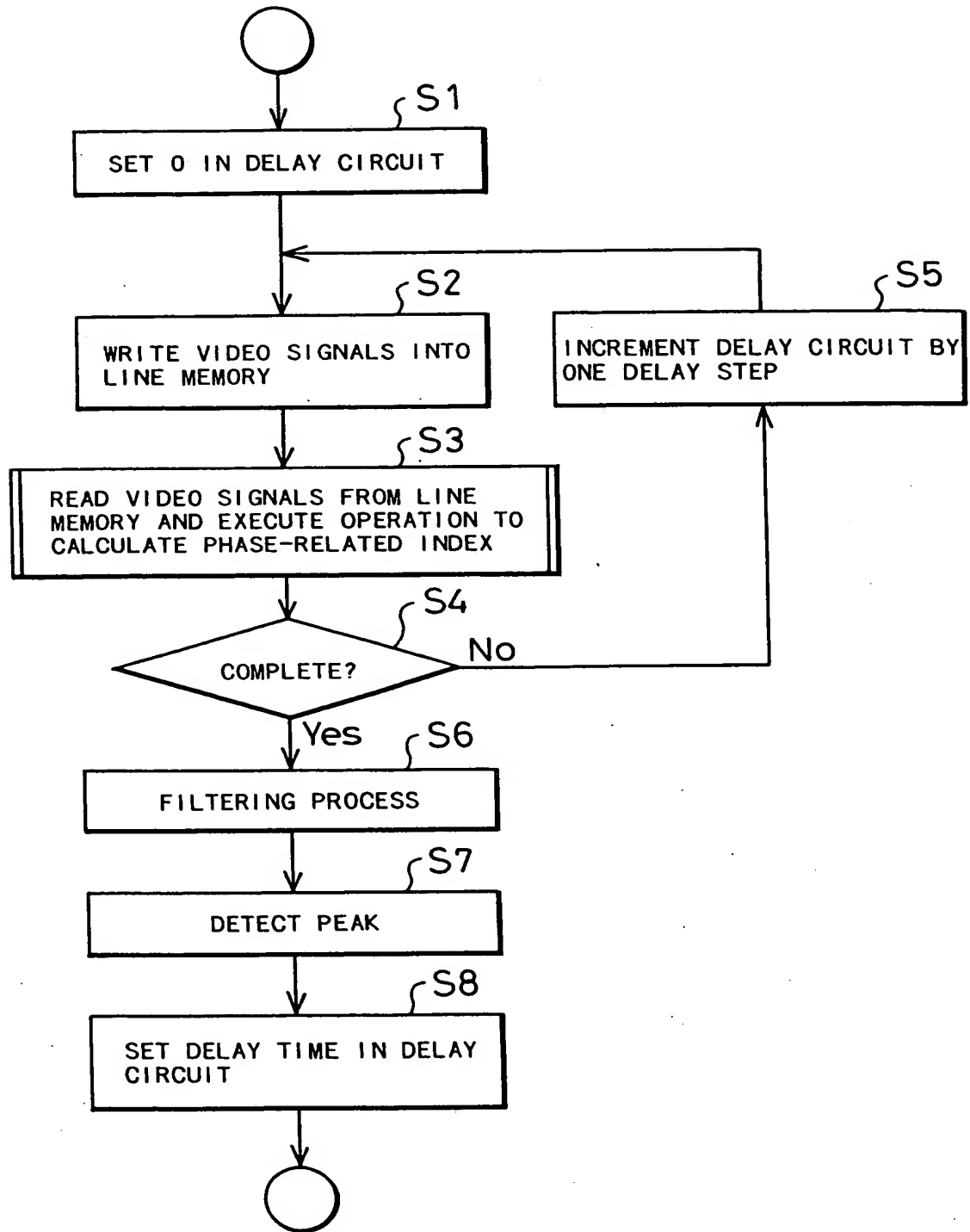


Fig. 5

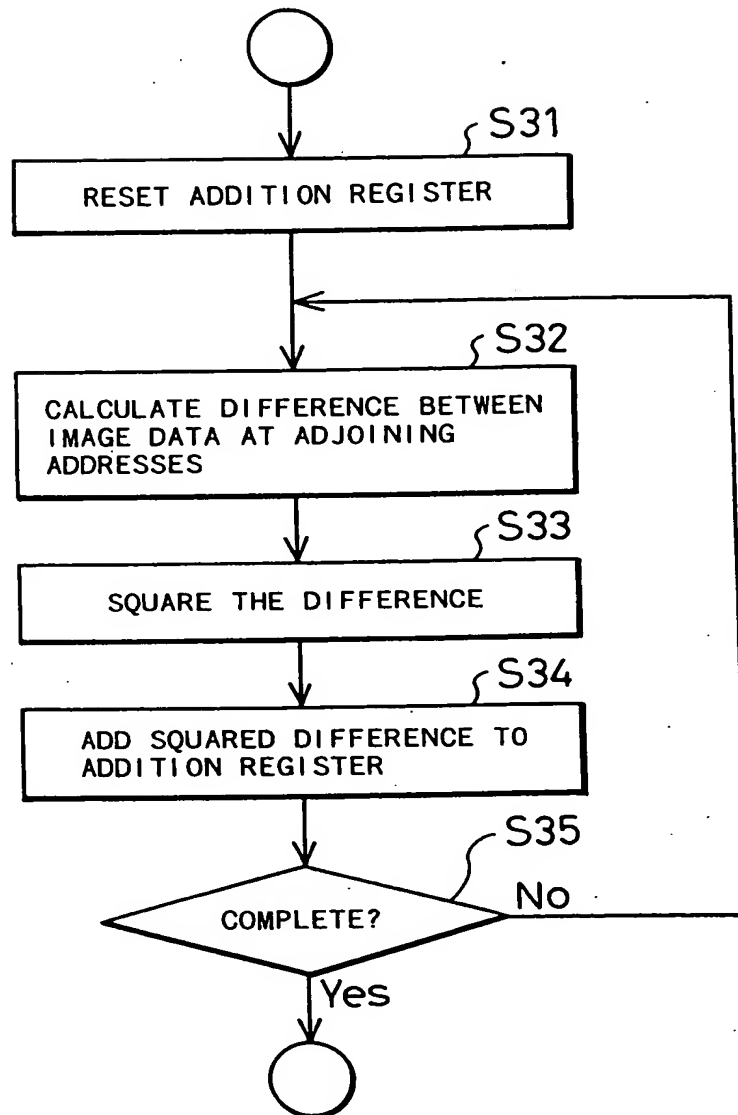


Fig. 6

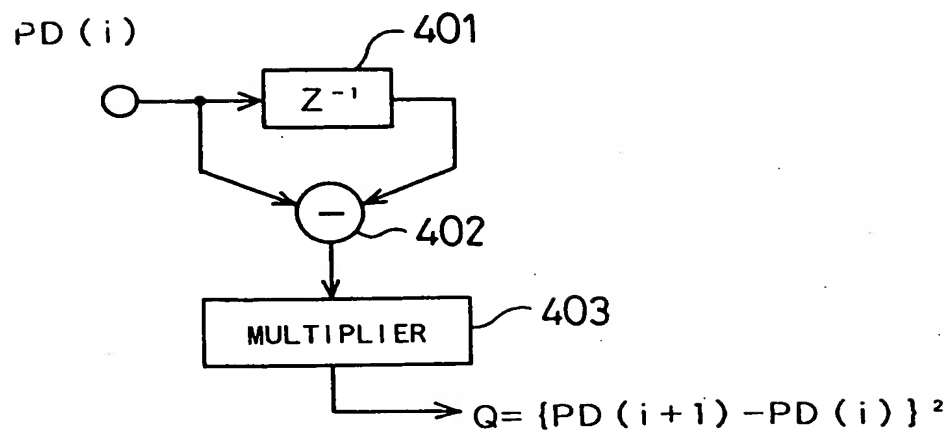
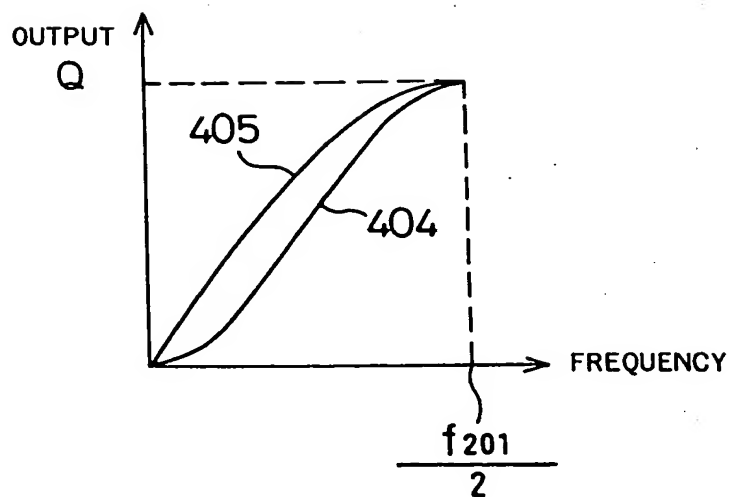


Fig. 7



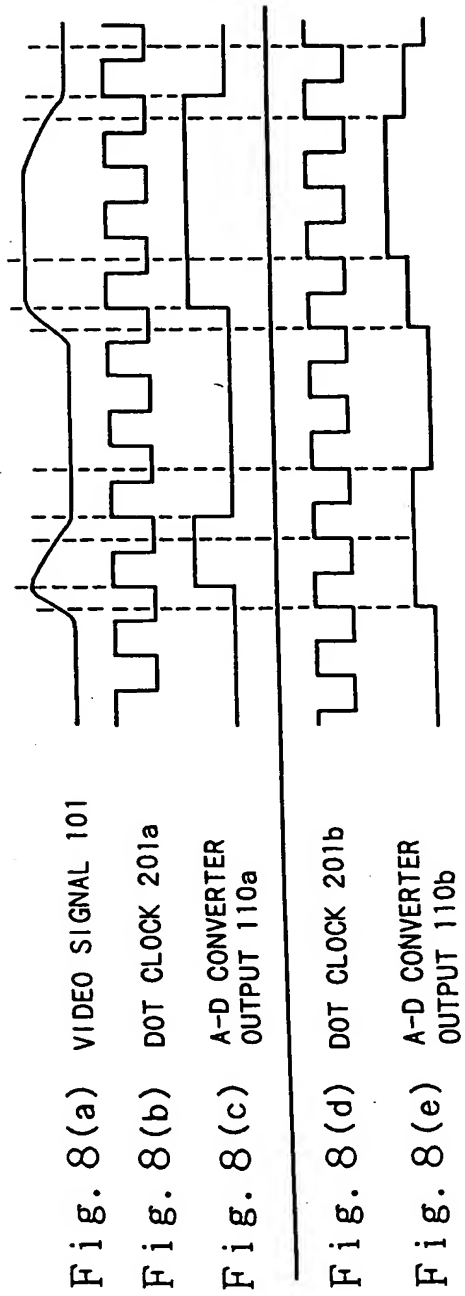


Fig. 9(a) LINE MEMORY  
OUTPUT 110a

Fig. 9(b) DELAY BY ONE  
CLOCK 221a

Fig. 9(c) DIFFERENCE 222a

Fig. 9(d) SQUARE 223a

Fig. 9(e) LINE MEMORY  
OUTPUT 110b

Fig. 9(f) DELAY BY ONE  
CLOCK 221b

Fig. 9(g) DIFFERENCE 222b

Fig. 9(h) SQUARE 223b

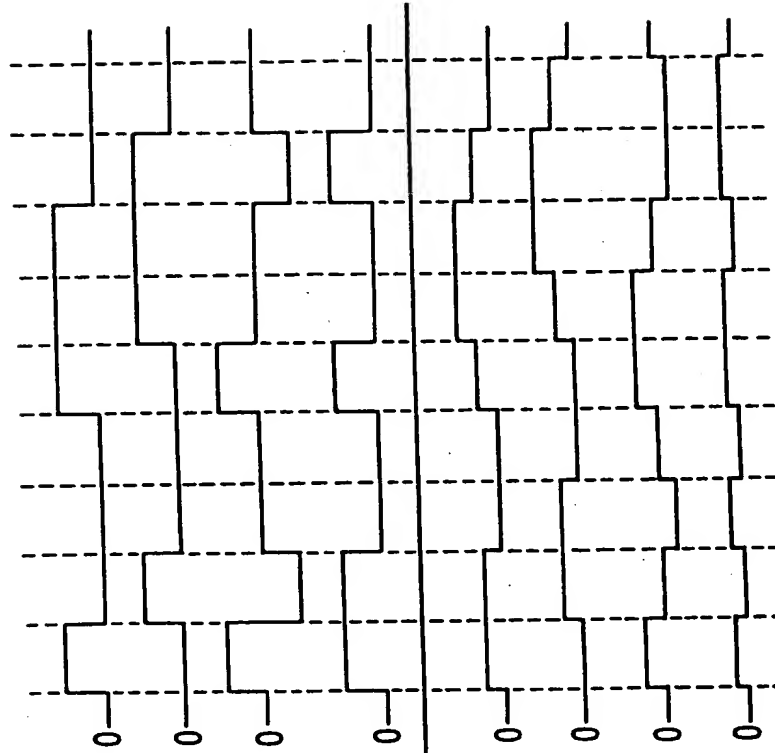




Fig. 10

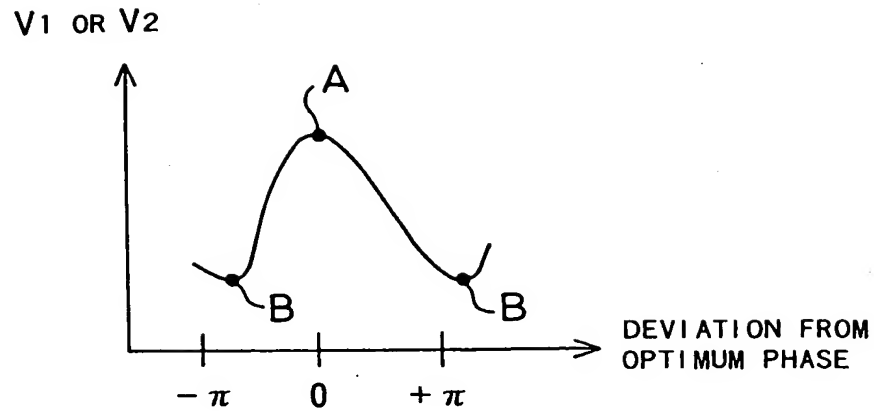


Fig. 11(A)

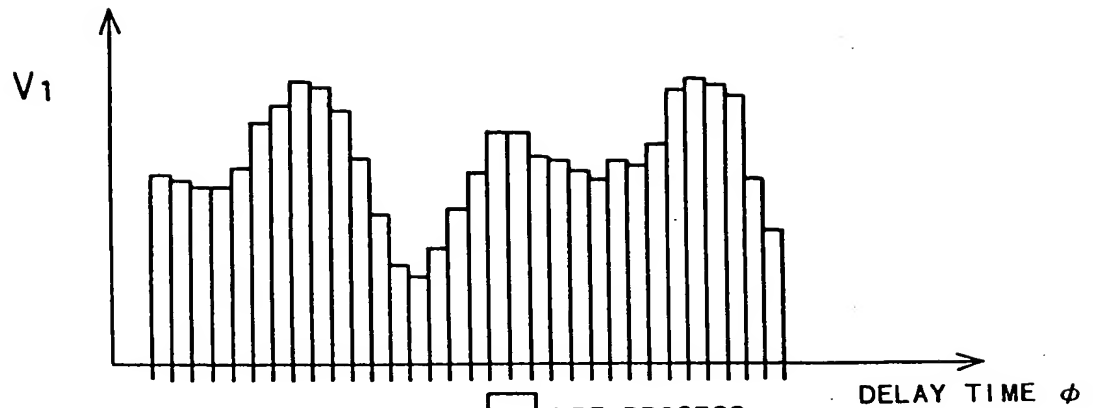


Fig. 11(B)

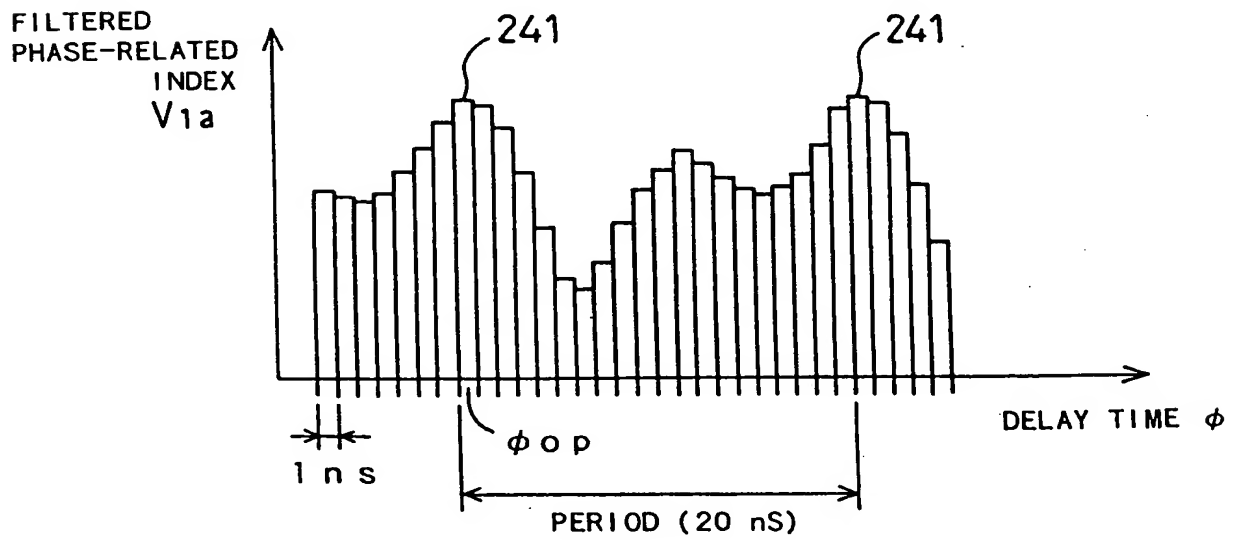


Fig. 12

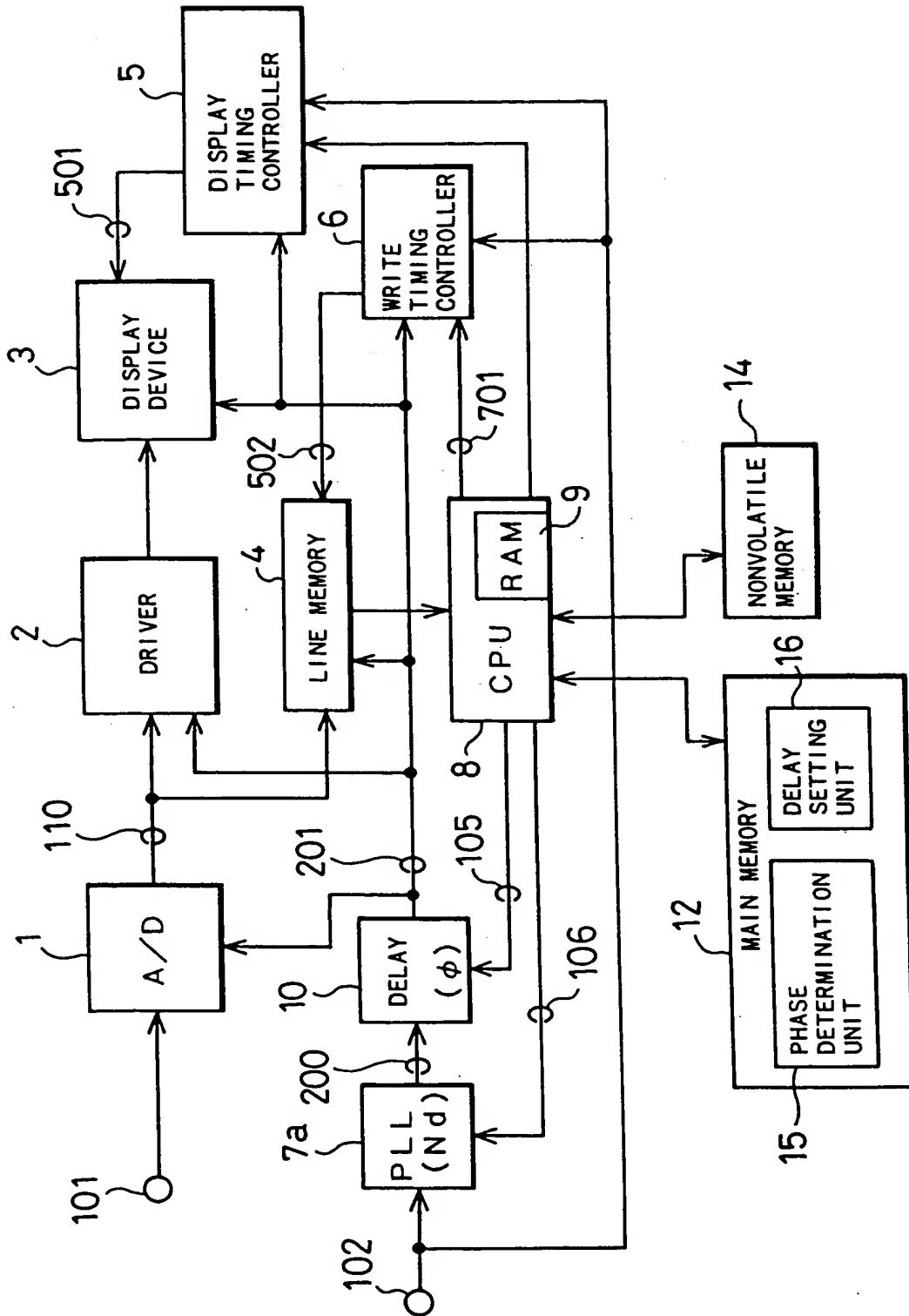


Fig. 13

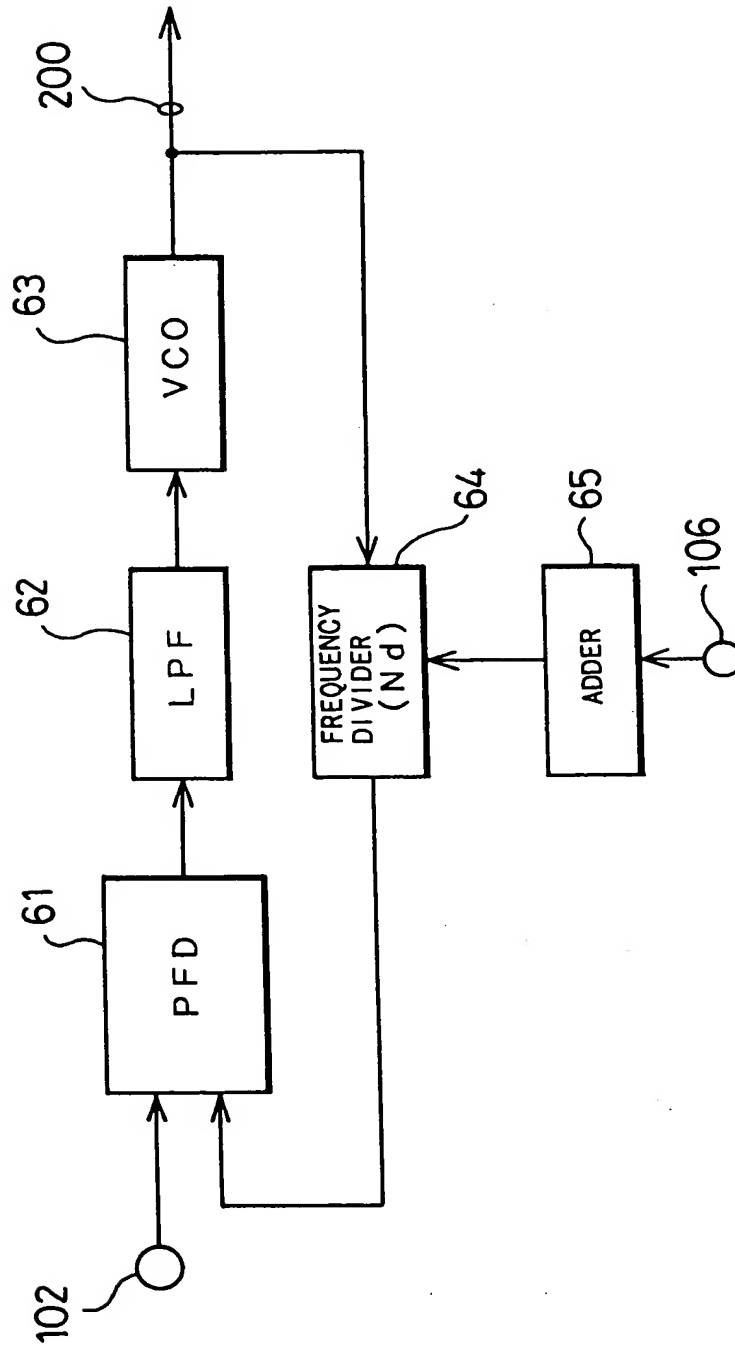


Fig. 14

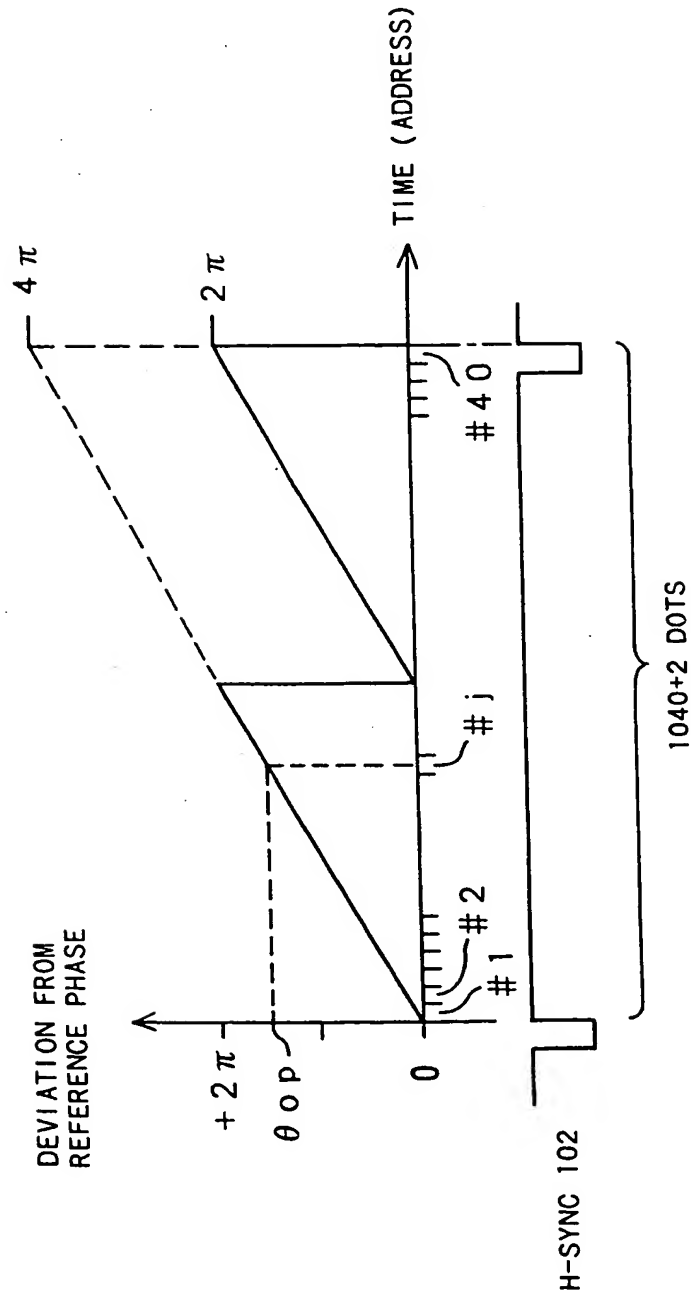


Fig. 15

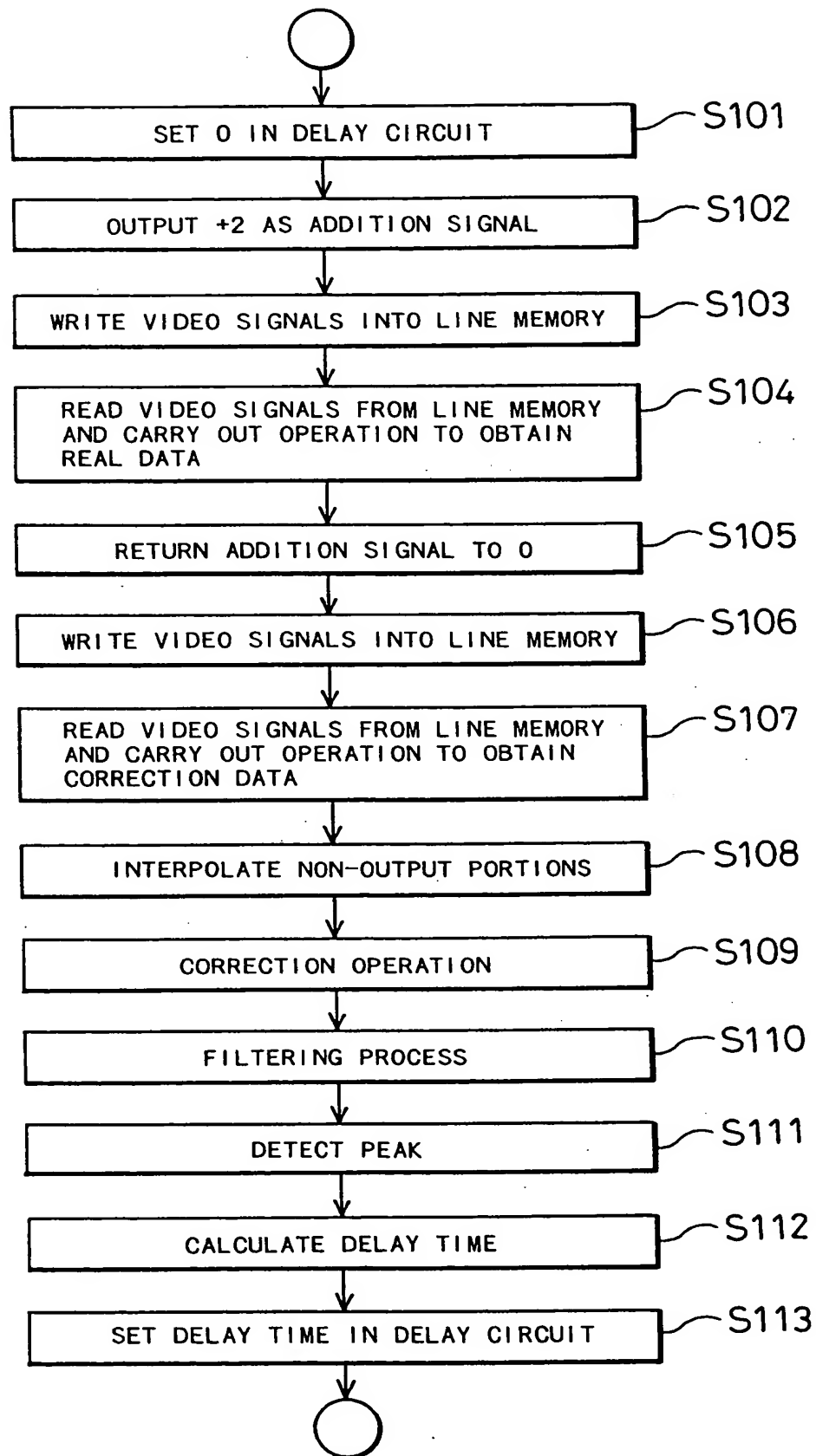


Fig. 16(A)

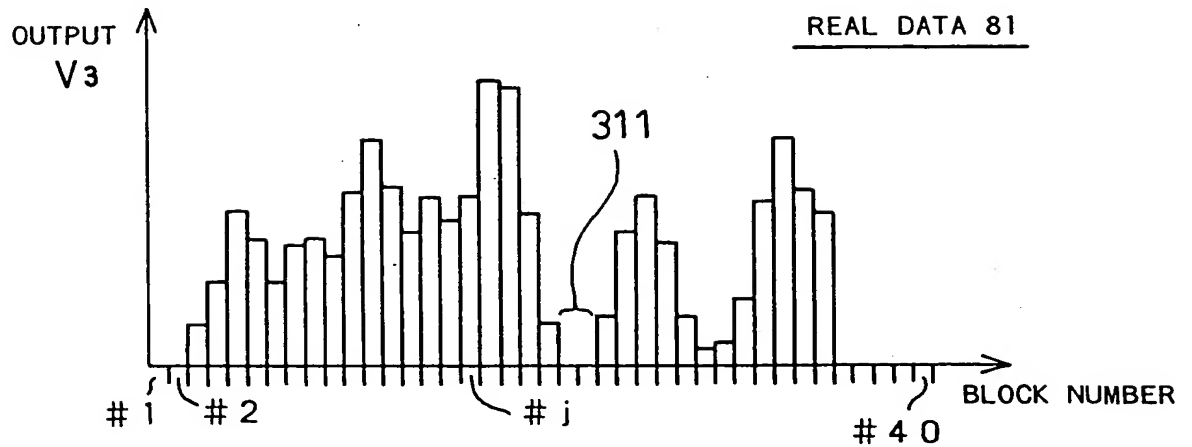


Fig. 16(B)

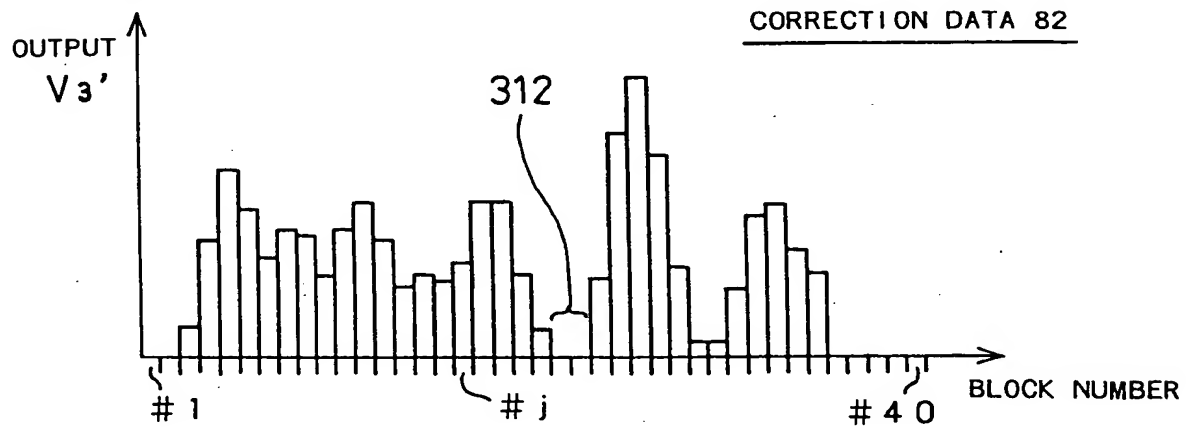


Fig. 16(C)

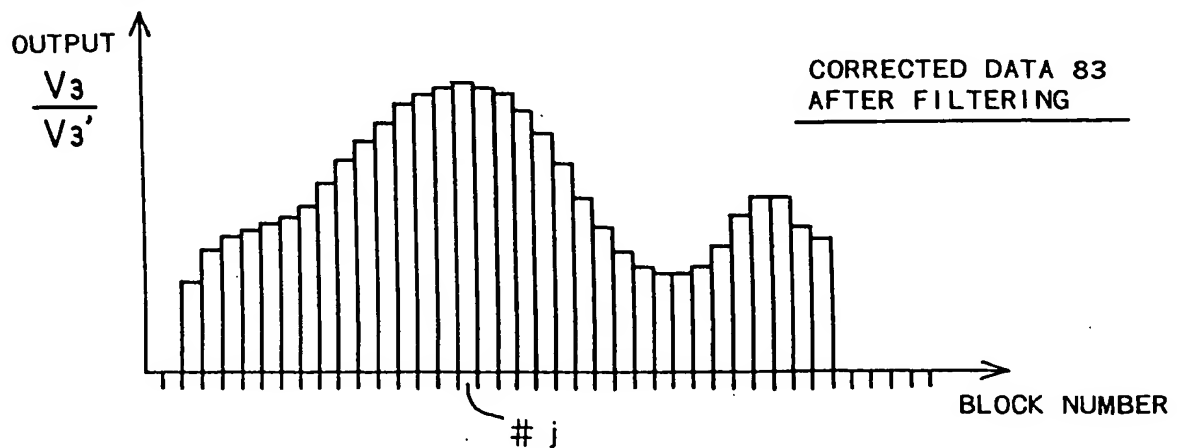
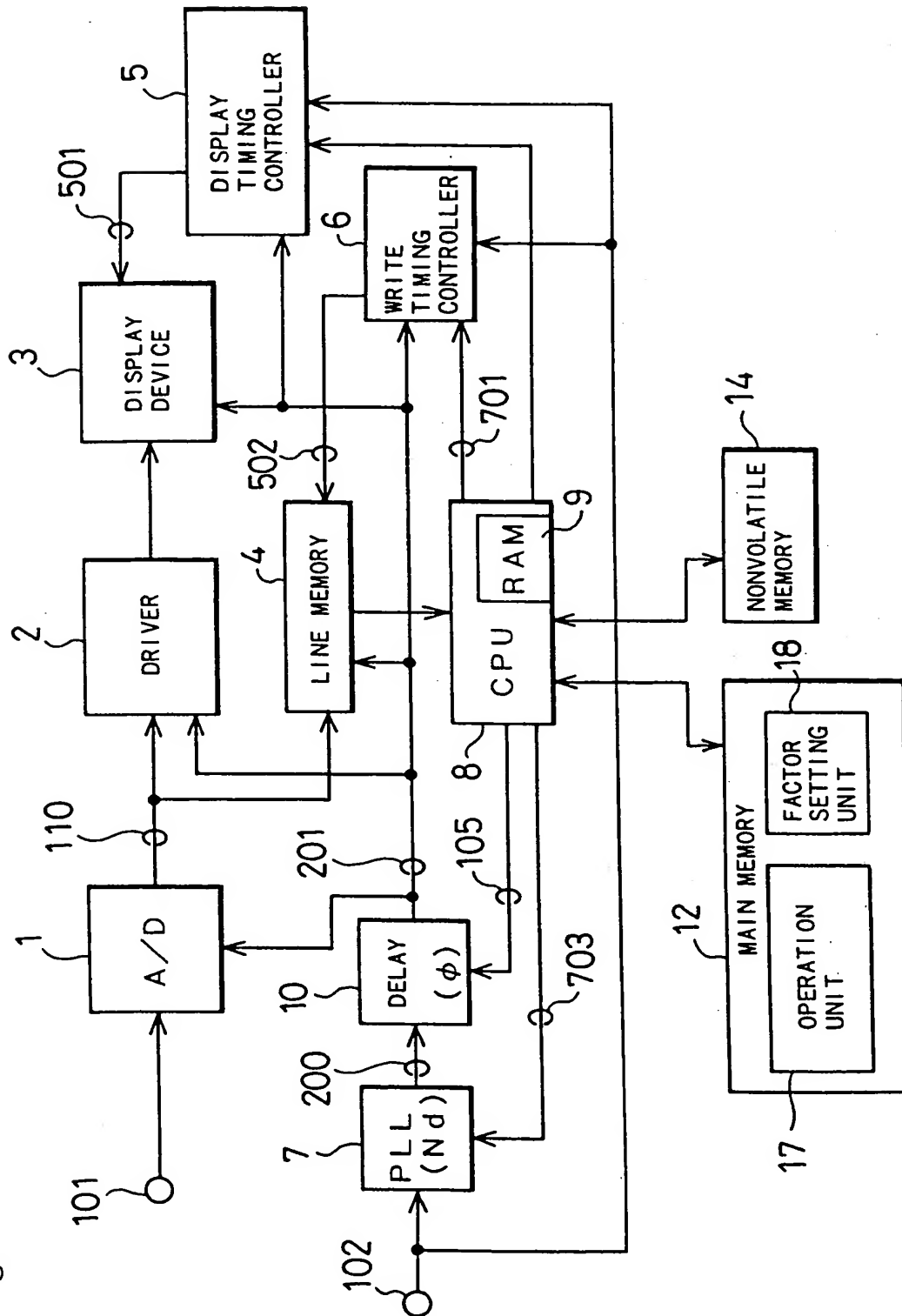


Fig. 17





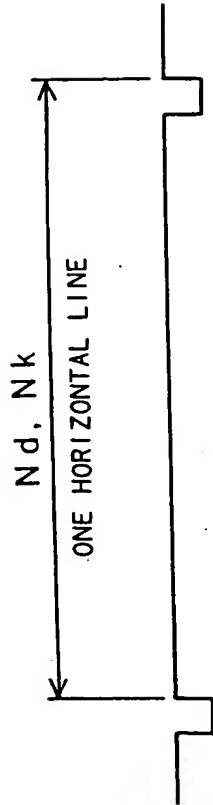


Fig. 18(a) H-SYNC 102

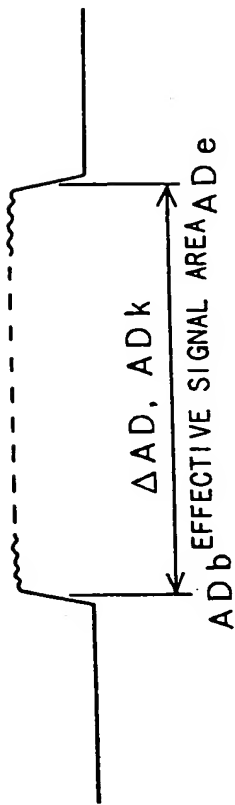
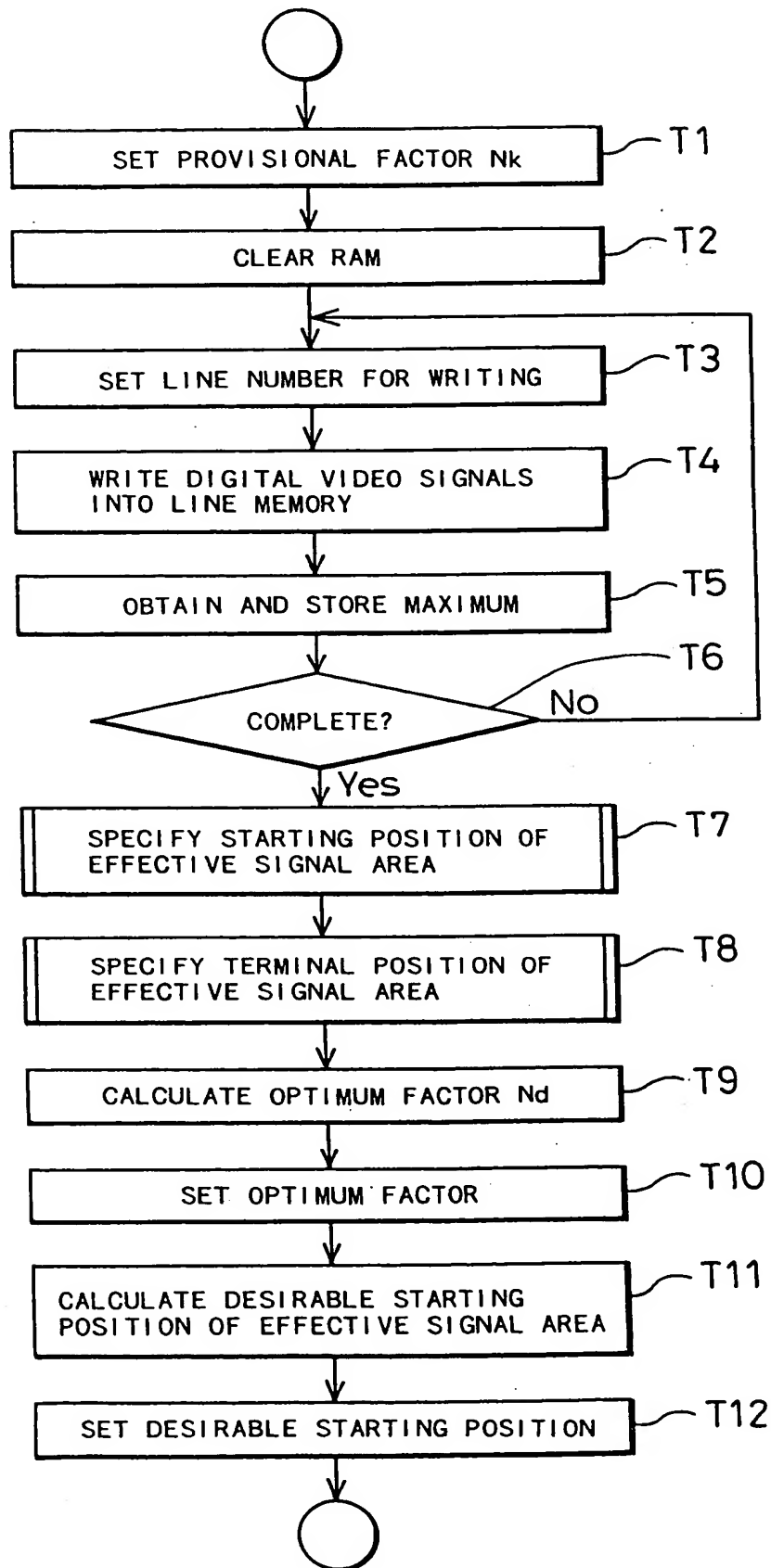


Fig. 18(b) VIDEO SIGNAL 101

$$\frac{N_d}{N_k} = \frac{\Delta AD}{\Delta AD_k}$$

$$N_d = \frac{\Delta AD}{\Delta AD_k} \times N_k$$

Fig. 19



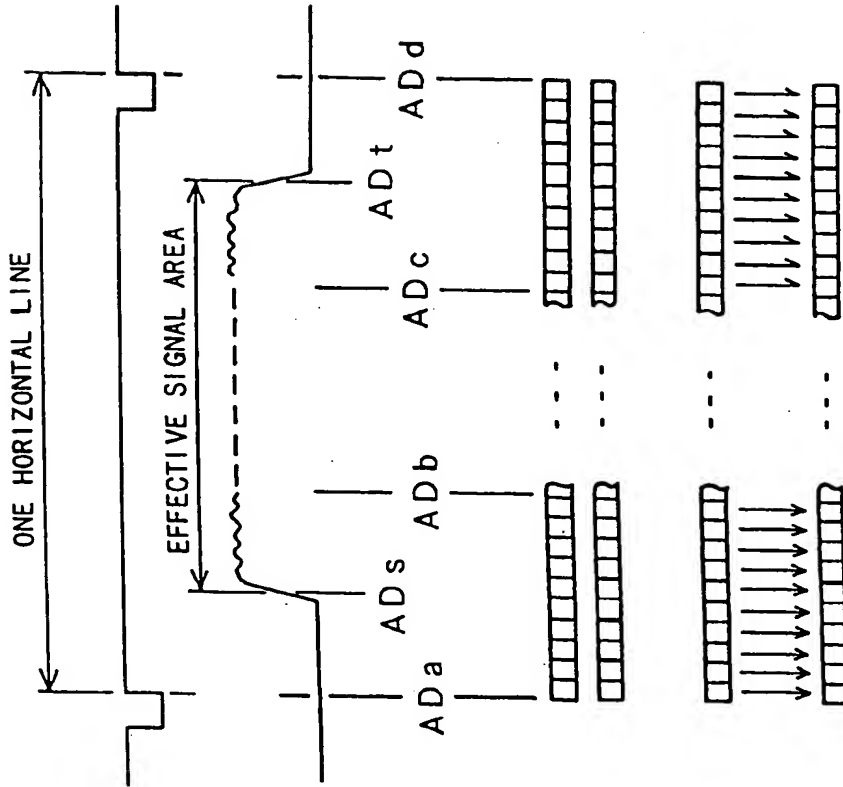


FIG. 20A

H-SYNC 102

FIG. 20B

VIDEO SIGNAL 101

FIG. 20C

IMAGE DATA FOR  
PLURAL LINES

FIG. 20D

MAXIMUM VALUES

Fig. 21

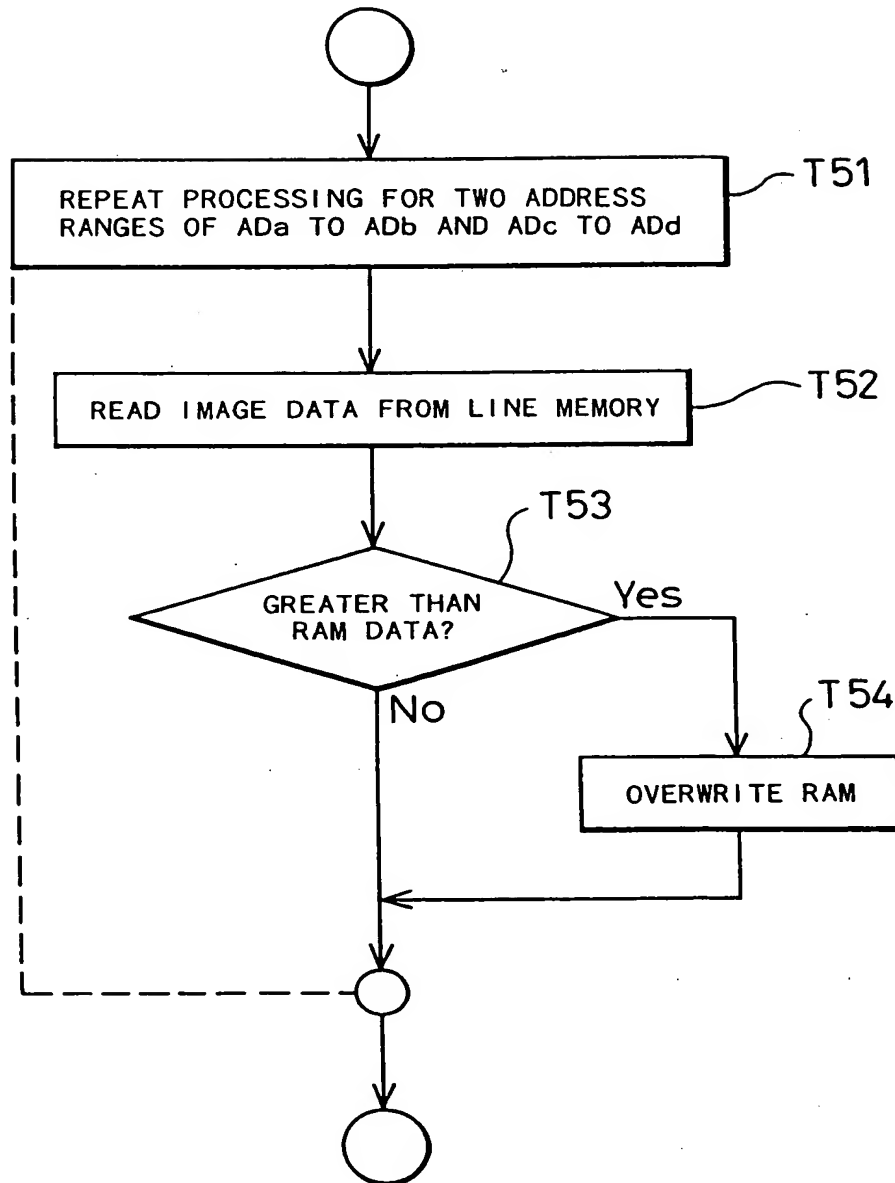


Fig. 22

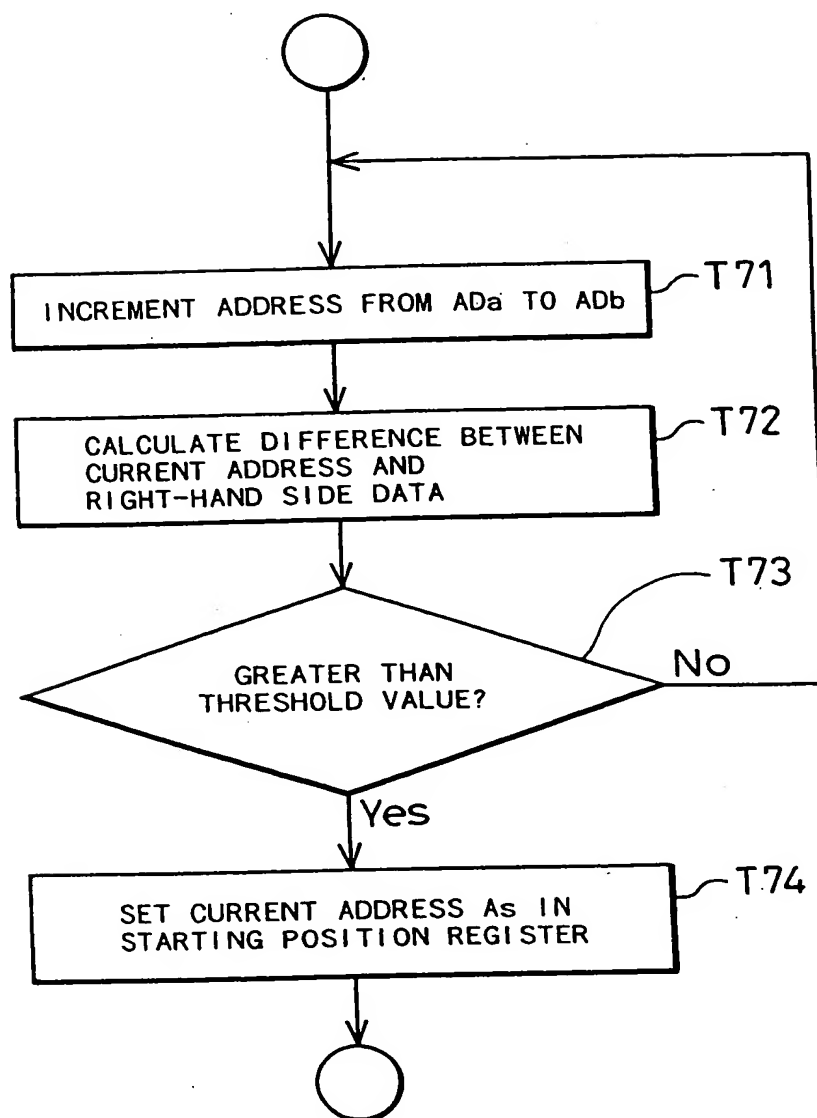


Fig. 23

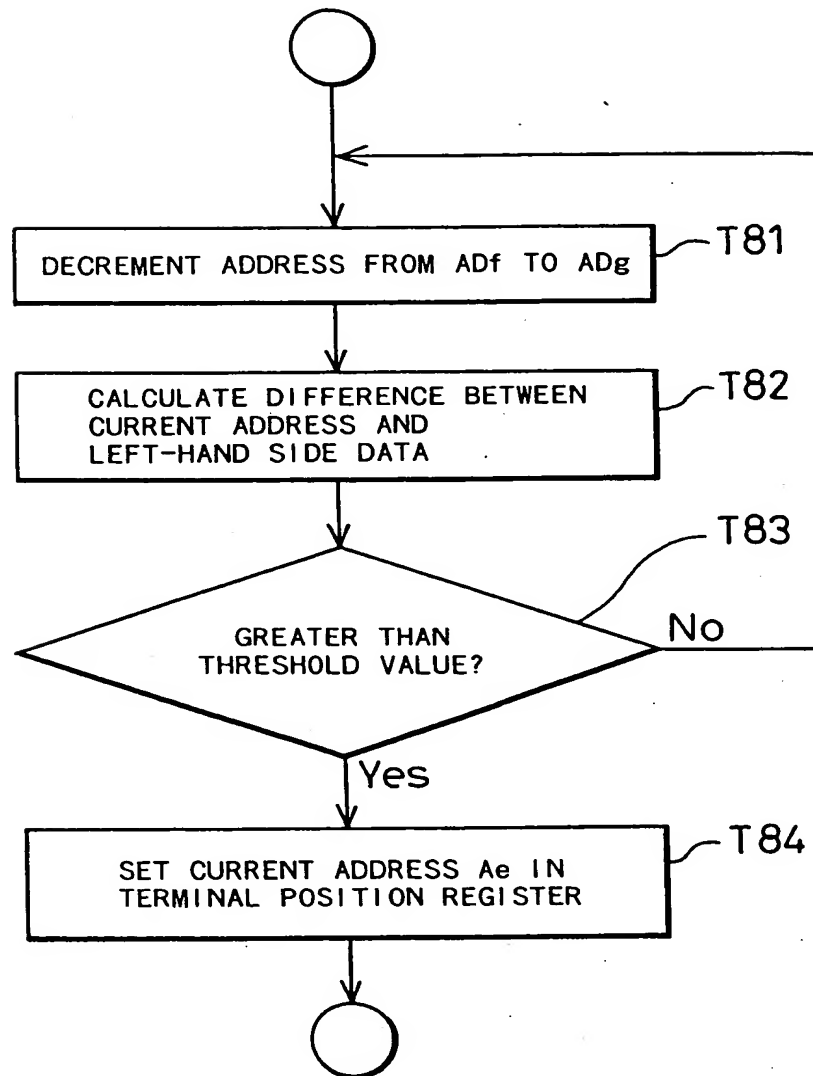


Fig. 24

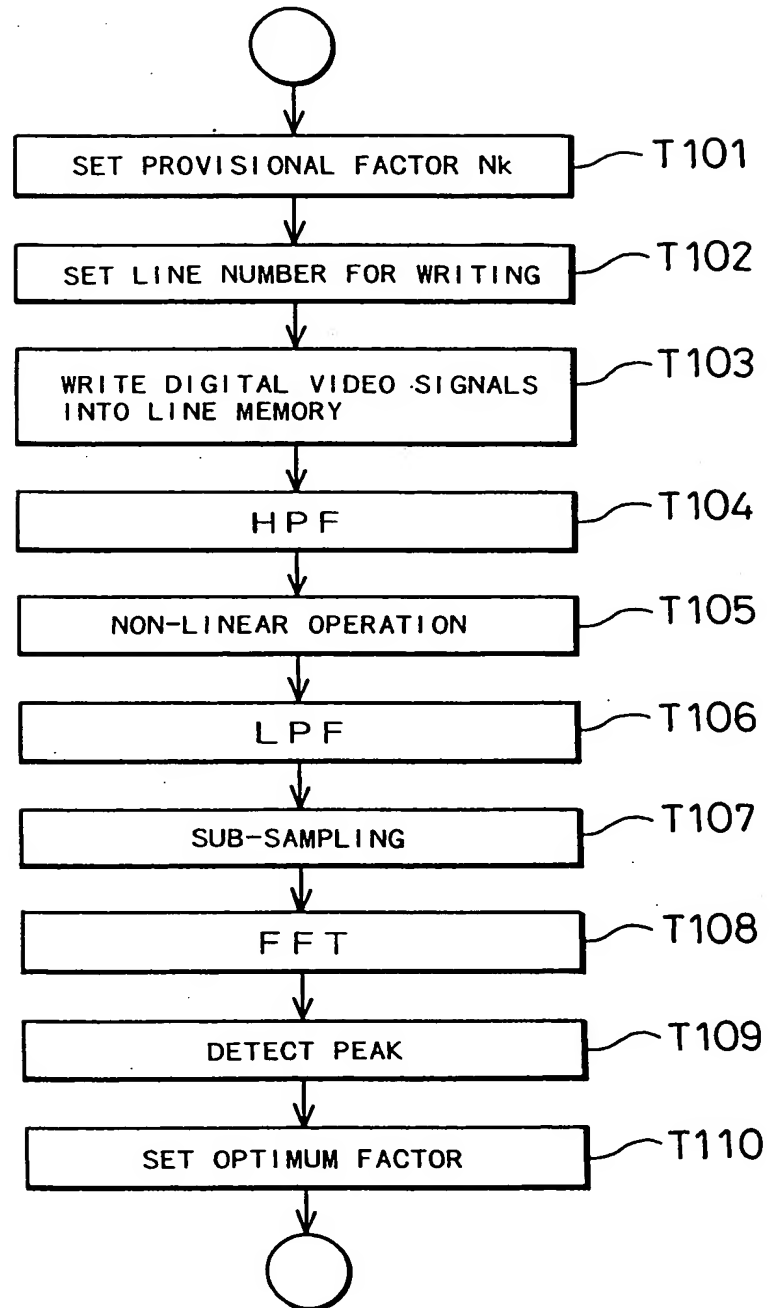


Fig. 25(a)

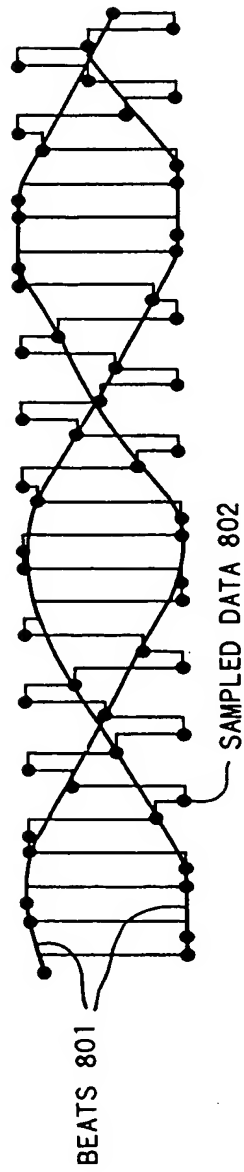


Fig. 25(b)

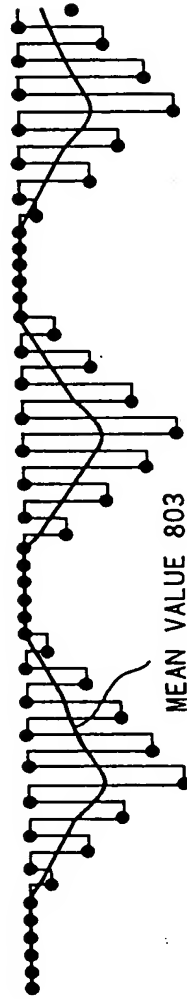




Fig. 26(a)

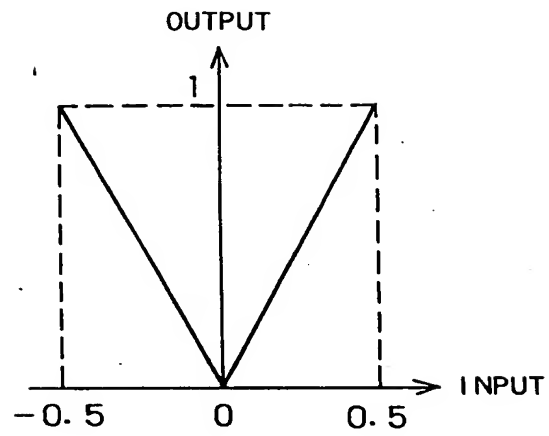


Fig. 26(b)

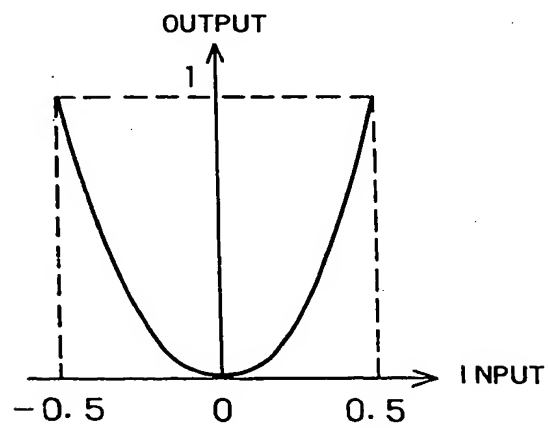


Fig. 27

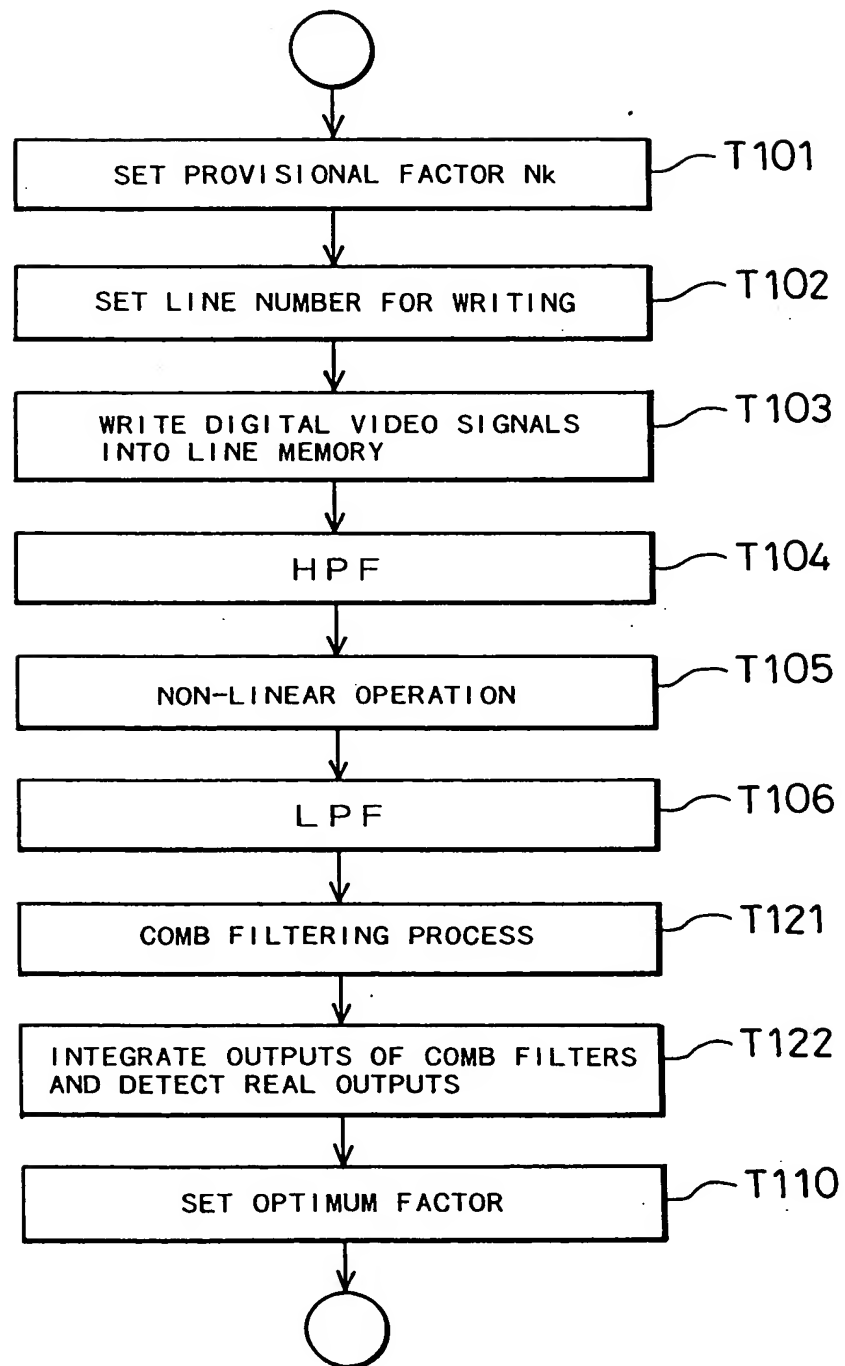


Fig. 28

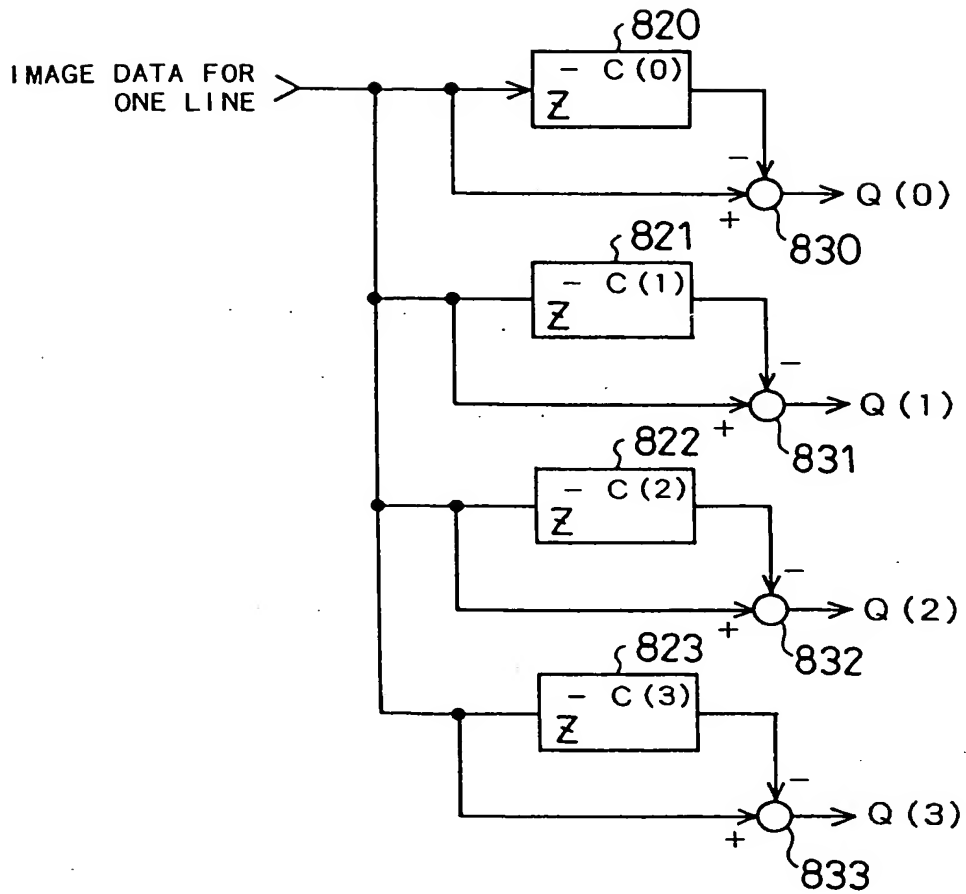


Fig. 29

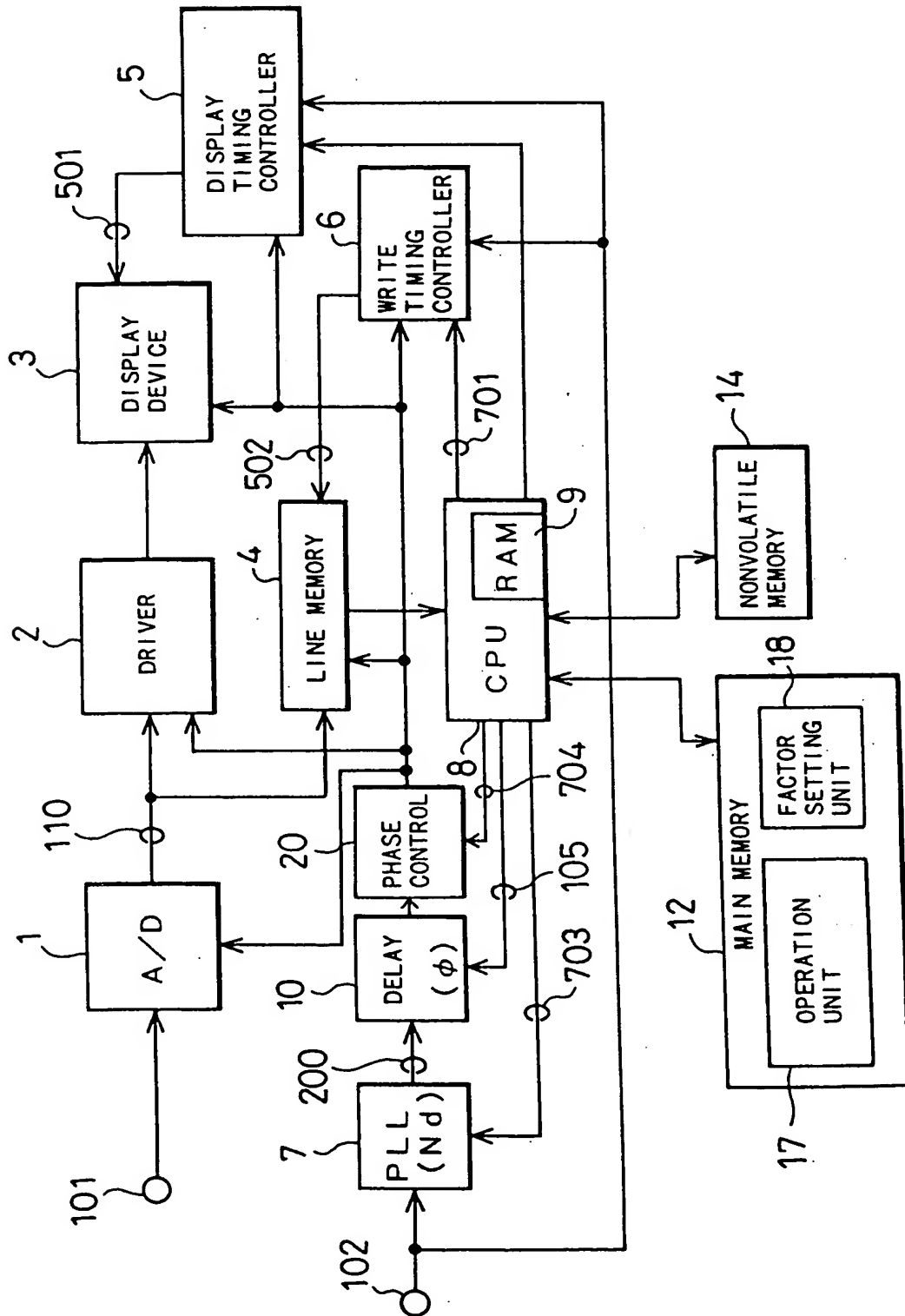


Fig. 30

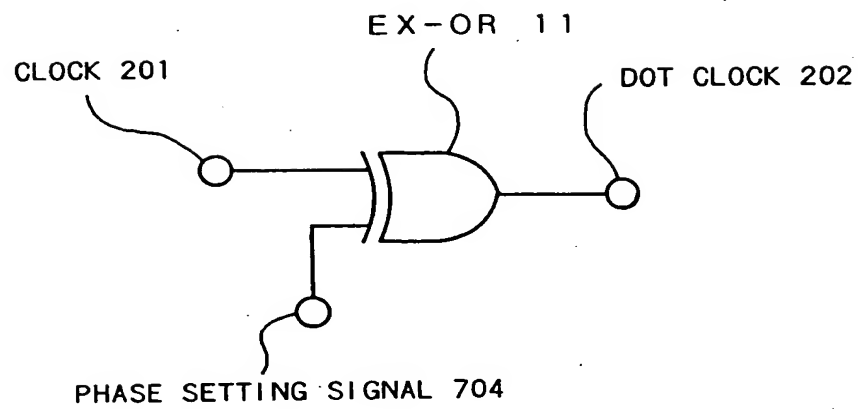


Fig. 31

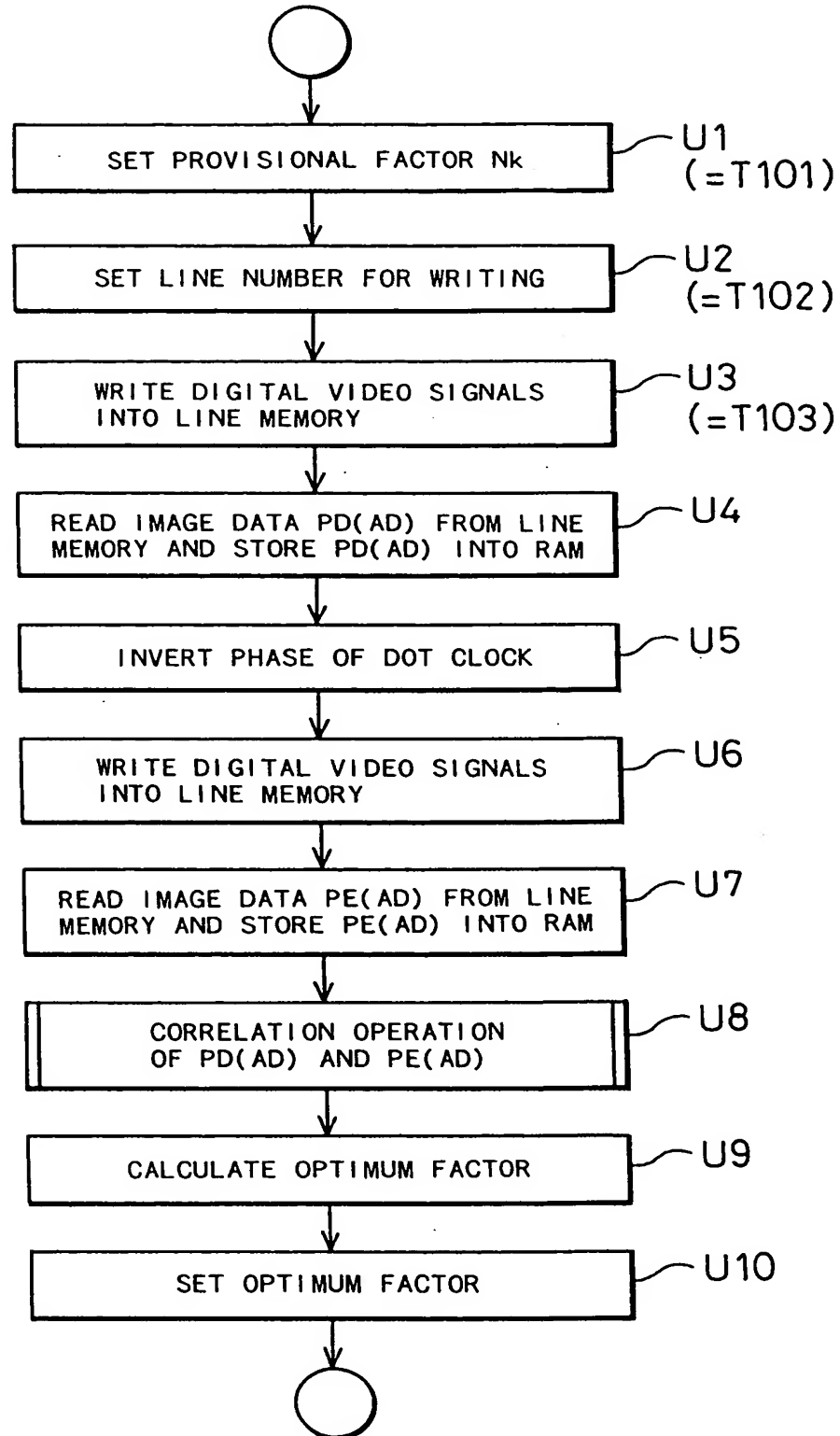


Fig. 32

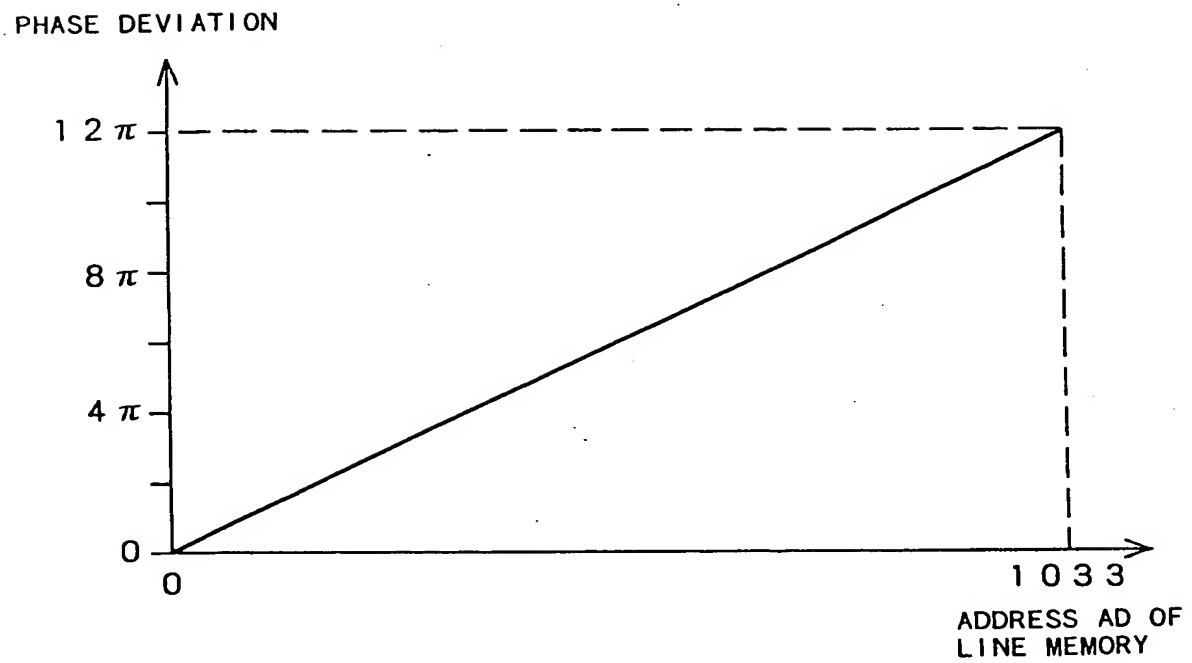


Fig. 33(a)

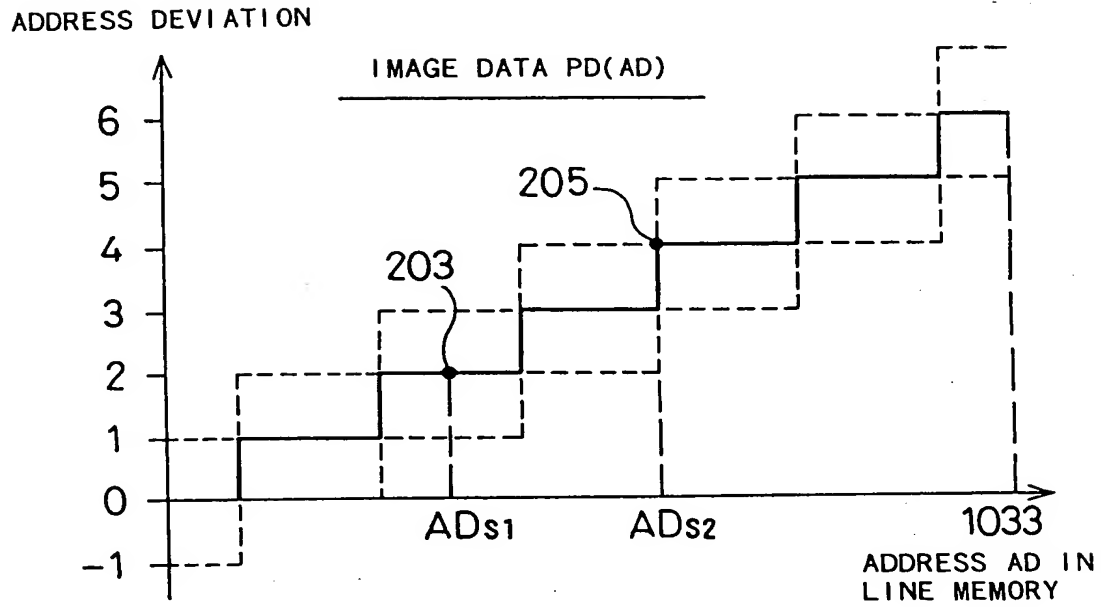


Fig. 33(b)

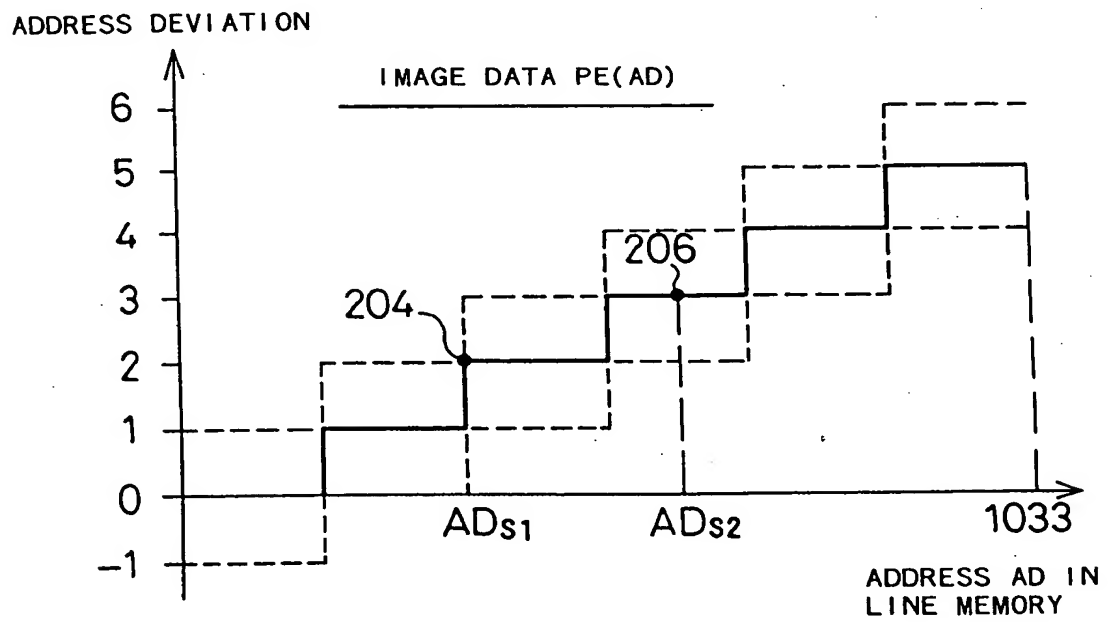




Fig. 34(a) PD (AD)

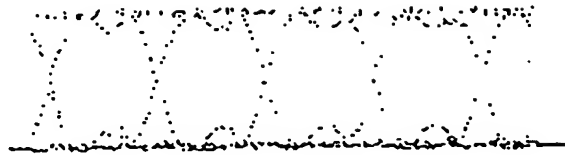


Fig. 34(b) PE (AD)

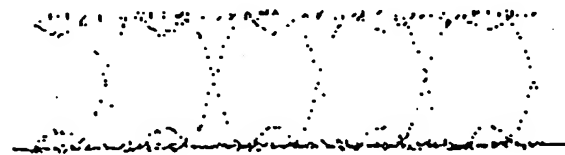


Fig. 34(c) A (AD)



Fig. 34(d) B (AD)



Fig. 34(e) C (AD)



Fig. 34(f) Q1 (AD)

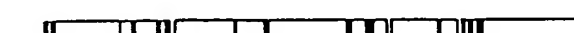


Fig. 34(g) Q2 (AD)



Fig. 35(a)

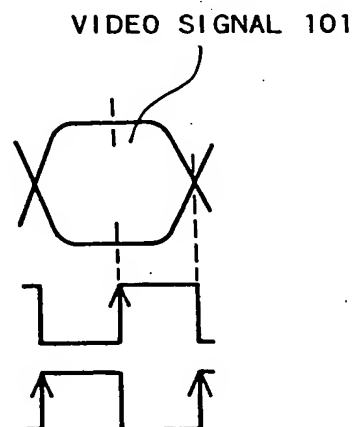
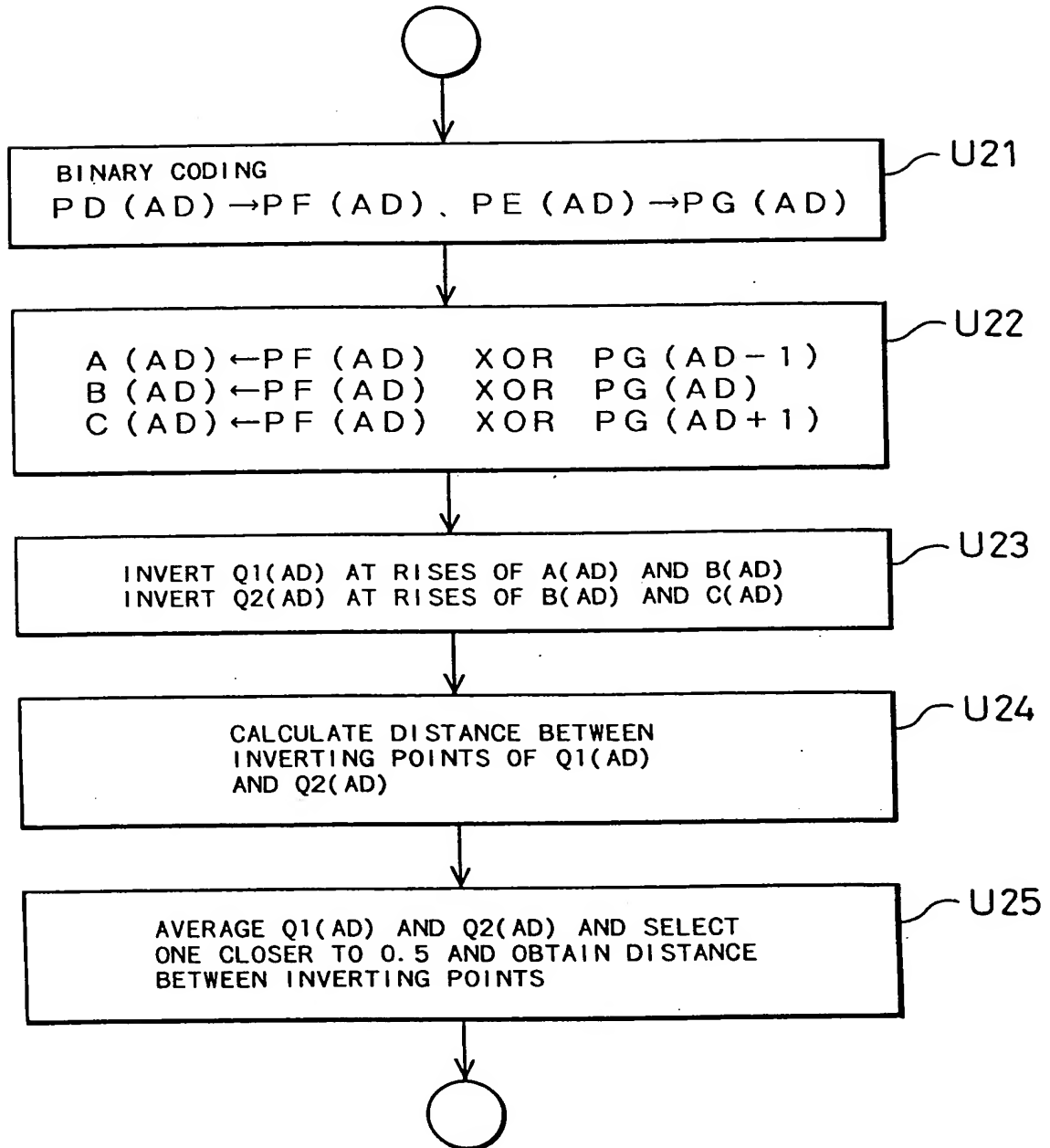


Fig. 35(b)

Fig. 35(c)

Fig. 36



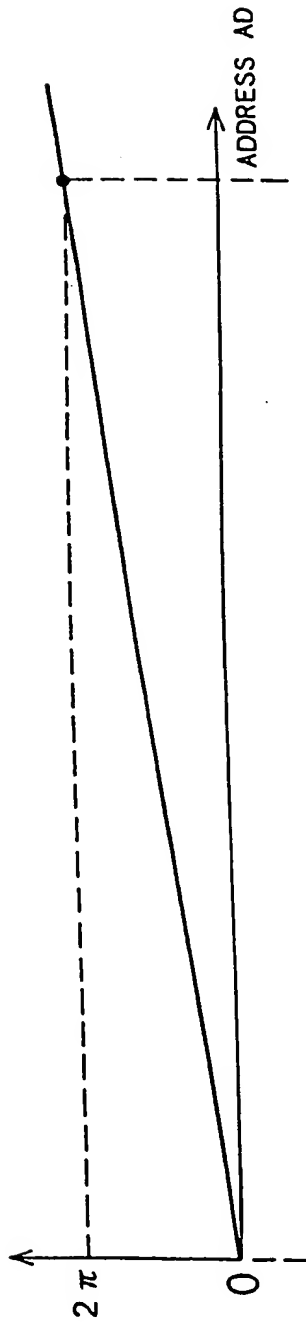


FIG. 37A  
PHASE DEVIATION

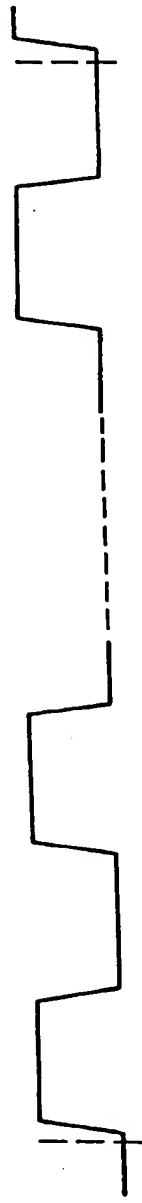


FIG. 37B  
VIDEO SIGNAL 101



FIG. 37C  
SAMPLING OF BINARY  
DATA PF(AD)

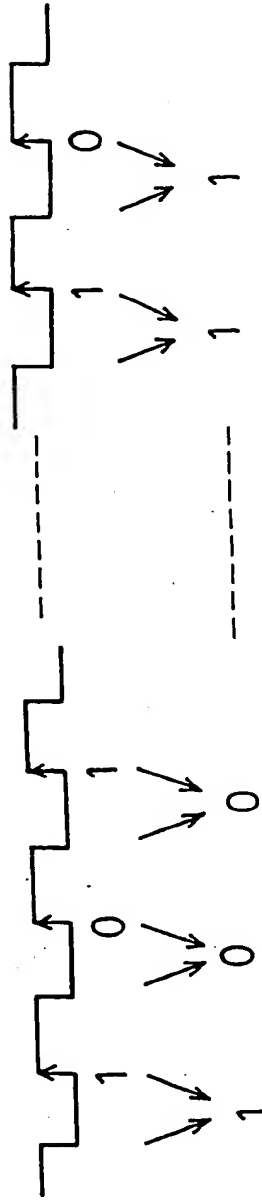


FIG. 37D  
SAMPLING OF BINARY  
DATA PG(AD)

FIG. 37E  
EX-OR

Fig. 38

INVERTING POINTS OF Q2 (AD)

NUMBER	INVERTING ADDRESS
# 1	6 2
# 2	8 7
# 3	1 7 3
# 4	2 6 4
# 5	3 4 8
# 6	4 3 0
# 7	5 1 1
# 8	6 0 5
# 9	6 8 0
# 1 0	7 7 9
# 1 1	8 5 3

Fig. 39

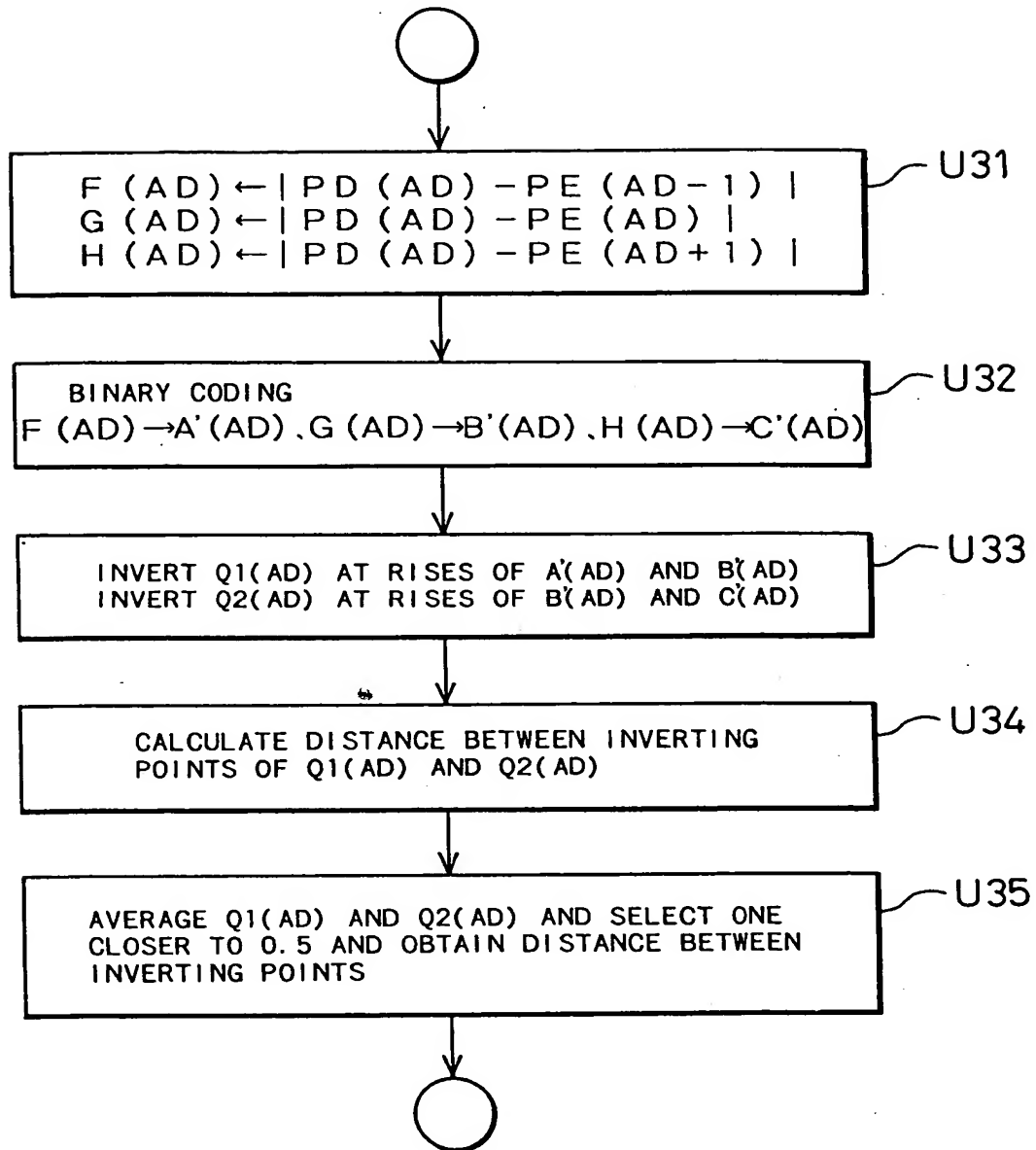
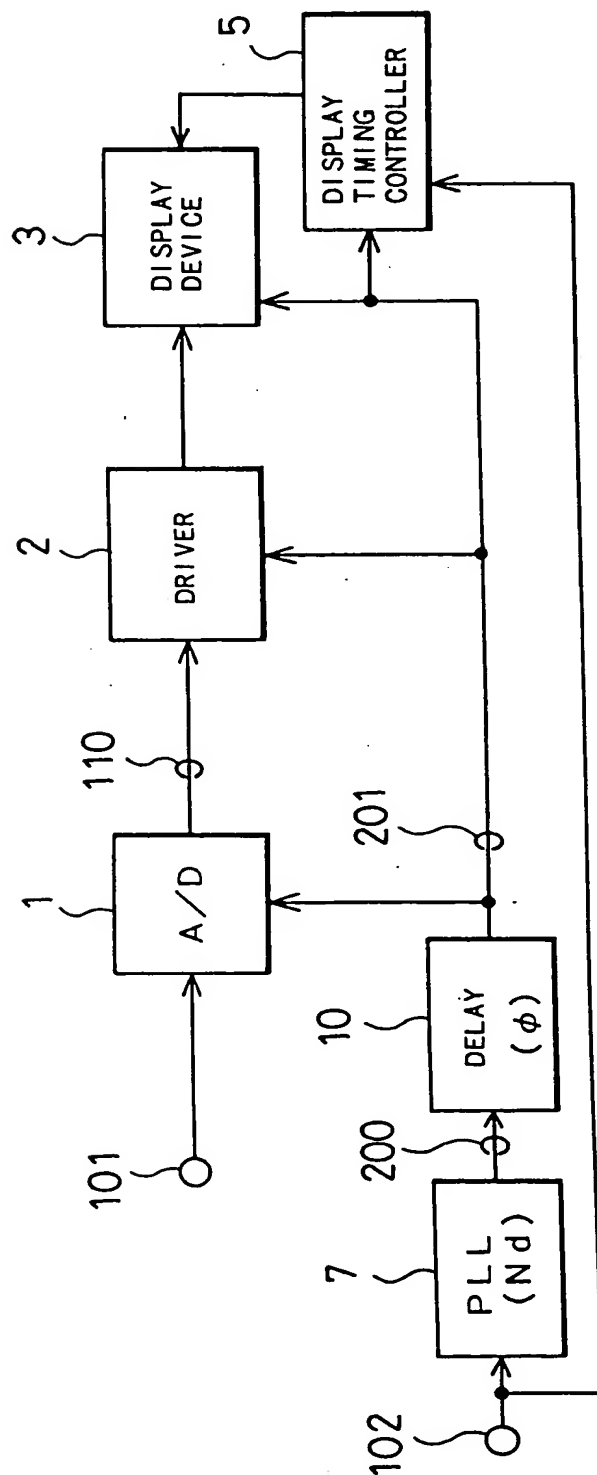
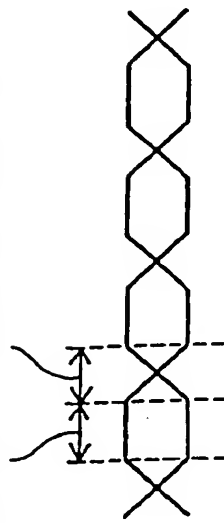


Fig. 40  
*PRIOR ART*



STABLE RANGE 121      TRANSIENT RANGE 122



*FIG. 41A*  
*PRIOR ART*

VIDEO SIGNAL 101

*FIG. 41B*  
*PRIOR ART*

DOT CLOCK 201A



*FIG. 41C*  
*PRIOR ART*

DOT CLOCK 201B



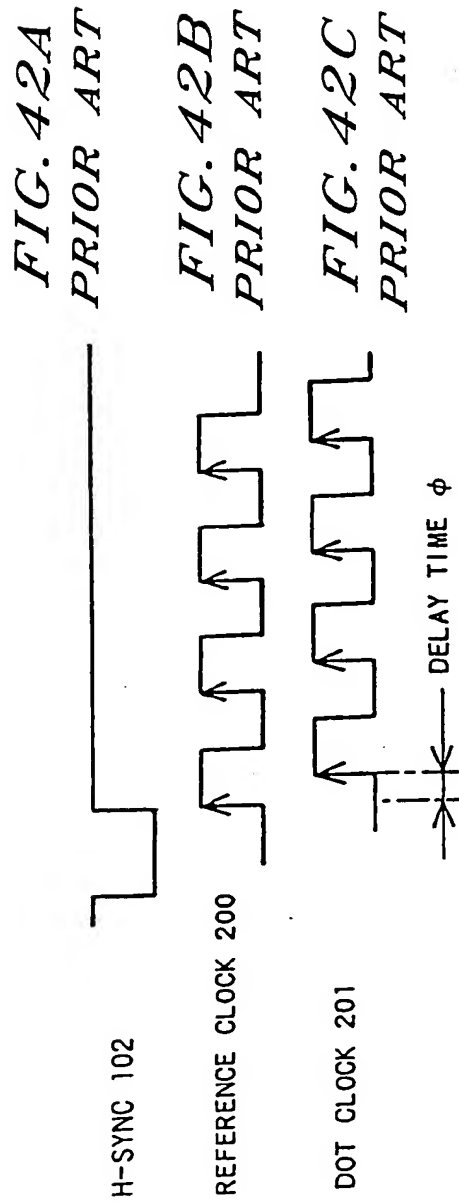
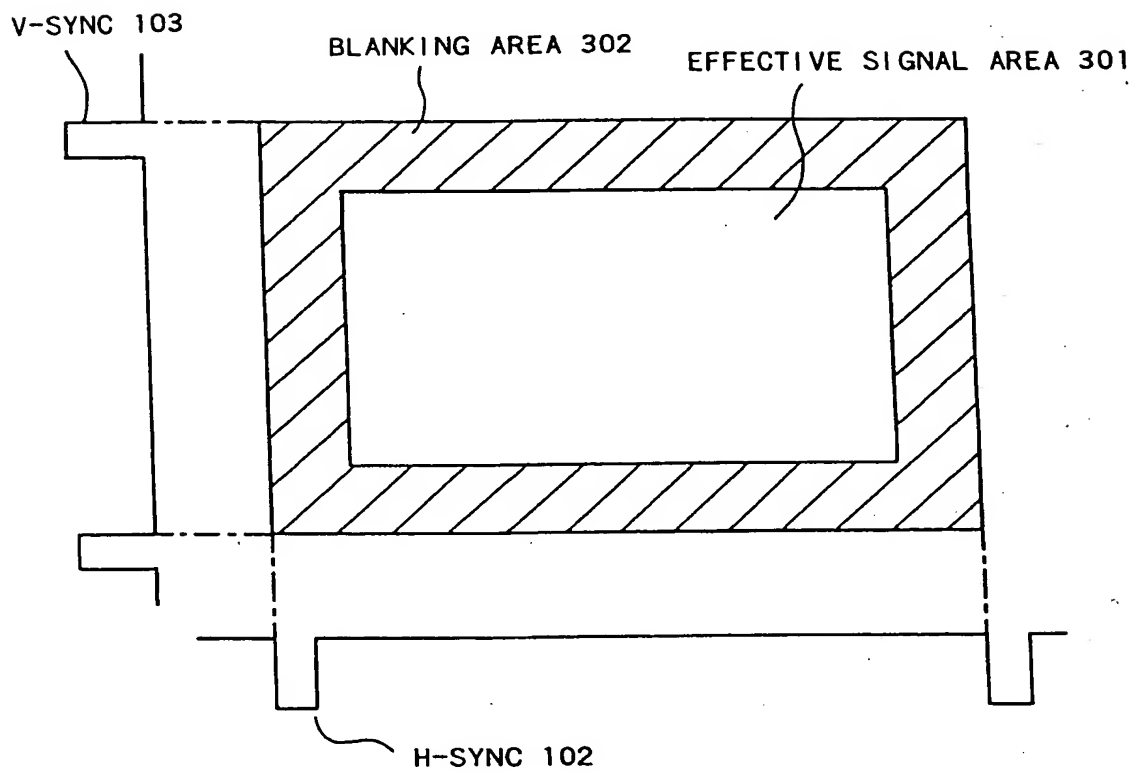
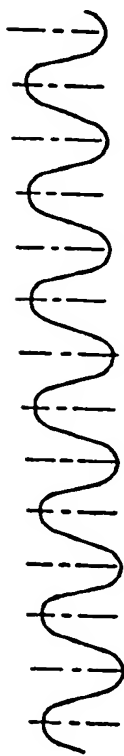




Fig. 43  
*PRIOR ART*



VIDEO SIGNAL 101



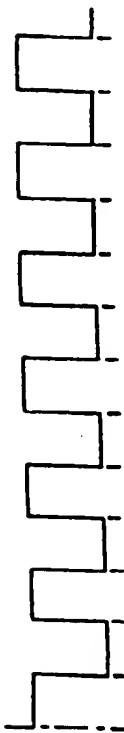
*FIG. 44A1*  
*PRIOR ART*

DOT CLOCK 201



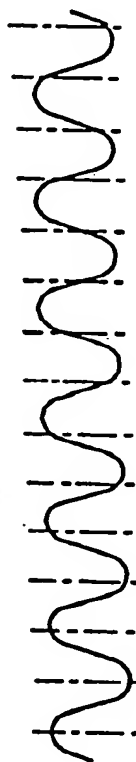
*FIG. 44A2*  
*PRIOR ART*

DIGITAL VIDEO SIGNAL 110  
(IN ANALOG FORM)



*FIG. 44A3*  
*PRIOR ART*

VIDEO SIGNAL 101



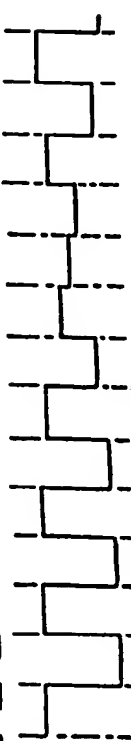
*FIG. 44B1*  
*PRIOR ART*

DOT CLOCK 201



*FIG. 44B2*  
*PRIOR ART*

DIGITAL VIDEO SIGNAL 110  
(IN ANALOG FORM)



*FIG. 44B3*  
*PRIOR ART*